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Cha	inge Type:											
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	Assembly Process			$\square$	Data She	et				Wafer	<sup>•</sup> Bump Ma	aterial
	Assembly Materials	s			Part num	iber cha	nge			Wafer	Bump Pro	ocess
	Mechanical Specifi	cation			Test Site					Wafer	Fab Site	
	Packing/Shipping/	Labelin	ng		Test Proc	cess					Fab Mate	
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				No	otificati	on De	tails					
	scription of Chang as Instruments Inco											
4	TEXAS INSTRUMENTS						ADC34	41, <b>A</b>	DC	3442, AC	DC3443, ADC	
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Ch	anges from Revision A (C	October 2	2015) to	Revi	sion B			SBAS	670E	3-JULY 201	4-REVISED APF	Page
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Cha •	Added description for avai	lability of	one-wire	e seri	al LVDS inter			ection				Page 1
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•	Changed specifications of Digital Outputs (LVDS Interface), VocM parameter in Digital Characteristics table	17
•	Changed rising to falling in description of SYSREF reference time parameter in Timing Requirements: General table	17
•	Changed Typical Characteristics sections: added dither on to all section condition statements, changed Non 23 to excluding HD2, HD3	19
	Added INL and DNL plots in Typical Characteristics: ADC3441 section	
	Changed conditions of Figure 34, Figure 35	
	Added INL and DNL plots in Typical Characteristics: ADC3442 section	
•	Changed conditions of Figure 67, Figure 68	31
•	Added INL and DNL plots in Typical Characteristics: ADC3443 section	36
•	Changed conditions of Figure 100, Figure 101	37
•	Added INL and DNL plots in Typical Characteristics: ADC3444 section.	42
•	Changed conditions of Figure 134	43
•	Added Figure 141 to Timing Diagrams section	44
•	Added Using the SYSREF Input section	50
•	Changed the description about synchronization of the phase of the divided clock in each device to the common sampling clock in Using the SYSREF Input section.	50
•	Added ADC3441 Power-Up Requirements section, deleted the Register Initialization section	57
•	Added last sentence to Detailed Design Procedure section of first typical application	75
•	Added Chopper On to caption of Figure 198	75
•	Added Chopper Off to caption of Figure 200	76
•	Changed the caption of Figure 202 from FFT for 450-MHz Input Signal (Dither On) to FFT for 450-MHz Input Signal (Chopper Off, Dither On)	77

## The datasheet number will be changing.

Device Family	Change From:	Change To:	
ADC3441, ADC3442, ADC3443,	SBAS670A	SBAS670B	
ADC3444			

These changes may be reviewed at the datasheet links provided. <u>http://www.ti.com/product/ADC3441</u>

## **Reason for Change:**

To accurately reflect device characteristics.

**Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):** 

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

## Changes to product identification resulting from this PCN:

None.

## **Product Affected:**

ADC3441IRTQR	ADC3441IRTQT	ADC3442IRTQR	ADC3442IRTQT
ADC3443IRTQR	ADC3443IRTQT	ADC3444IRTQR	ADC3444IRTQT

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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