molex

PRODUCT SPECIFICATION

DDR4 DIMM SOCKET

1.0 SCOPE

This Product Specification covers the 0.85mm centerline gold plated DDR4 DIMM edge card connector for 1.40 ± 0.10 thick memory modules.

2.0 PRODUCT DESCRIPTION

2.1 PRODUCT NAME AND SERIES NUMBER(S) <u>Series Number</u> 151080 DDR4DIMM 25° THROUGHHOLE 0.76Au 288CKT

2.2 DIMENSIONS, MATERIALS, PLATINGS AND MARKINGS

See the appropriate sales drawing(s) for information on dimensions, materials, plating and markings, on recommended module outlines and footprint specifications.

2.3 SAFETY AGENCY APPROVALS

- UL File Number TBA
- CSA File Number TBA



molex

PRODUCT SPECIFICATION

3.0 APPLICABLE DOCUMENTS AND SPECIFICATIONS

Refer to the appropriate sales drawing(s) and other sections of this specification for the necessary referenced documents and specifications. In the event of conflict between the requirements of this specification and the product drawing(s), the product drawing(s) shall take precedence. In the event of conflict between the requirements of this specification and the reference documents, this specification shall take precedence.

4.0 RATINGS

4.1 VOLTAGE

29 Volts AC (RMS) / DC

4.2 CURRENT

0.75 Amps / pin

4.3 TEMPERATURE

Operating: - 55°C to + 85°C Nonoperating: - 55°C to + 85°C

4.4 FIELD LIFE AND TEMPERATURE

Field life: 5 Years Field temperature: 65°C

5.0 PERFORMANCE

5.1 ELECTRICAL REQUIREMENTS

ITEM	DESCRIPTION	TEST CONDITION	REQUIREMENT
1	Low Level Contact Resistance (LLCR) (Initial)	EIA-364-23 – Option 1 Mate connectors: apply a current of 100 mA maximum and voltage of 20 mV maximum.	20 milliohms MAXIMUM
2	Low Level Contact Resistance (LLCR) (Change from initial)	EIA-364-23 – Option 1 Mate connectors. Apply a current of 100 mA maximum and a voltage of 20 mV maximum.	10 milliohms MAXIMUM
3	Insulation Resistance	EIA-364-21 Unmate and unmount connectors: apply a voltage of 500 VDC between adjacent terminals and between terminals to ground.	1 Megaohms MINIMUM
4	Dielectric Withstanding Voltage	EIA-364-20 – Method B Unmate connectors: apply a voltage of 500 VAC for 1 minute between adjacent terminals.	No breakdown

5.2 MECHANICAL REQUIREMENTS

ITEM	DESCRIPTION	TEST CONDITION	REQUIREMENT
5		EIA-364-13 – Method B Mate a 1.50 ± 0.01 mm thickness gauge (GS-010-1) to the connector at a rate of 25.4 mm per minute.	106.8 N MAXIMUM

REVISION:	ECR/ECN INFORMATION:	TITLE: PRODU	TLE: PRODUCT SPECIFICATION					
2	EC No: S2015-0772	DDR4 D	DDR4 DIMM, 0.85mm PITCH					
2	<u>DATE:</u> 2014/11/20	25°, TH	2 of 7					
DOCUMENT NUMBER:		CREATED / REVISED BY:	CHECKED BY:	BY: APPROVED B				
PS-151080-0001		CC TEH	CG TAN	SH LENI				
TEMPLATE FILENAME: PRODUCT_SPECI					C[SIZE_A4](V.1).DOC			



PRODUCT SPECIFICATION

5.2 MECHANICAL REQUIREMENTS

6 7 8 9	Retention Force - TerminalRetention Force - Fork lockDurability (Preconditioning)Durability	EIA-364-29 – Method C Axial pull-out of terminal in the maximum rate of 25.4 mm per EIA-364-29 – Method C Axial pull-out of fork lock in th maximum rate of 25.4 mm pe EIA-364-09 Perform 5 plug-and-unplug cy maximum rate of 5 cycles per EIA-364-09 Perform 25 plug-and-unplug cy maximum rate of 5 cycles per EIA-364-28	e housing at a r minute. vcles at a r minute. cycles at a	13 No e	00gf MINIM per pin 3.3 N MINIM per fork loc vidence of p damage	1UM xk
8	Fork lock Durability (Preconditioning)	Axial pull-out of fork lock in th maximum rate of 25. 4 mm pe EIA-364-09 Perform 5 plug-and-unplug cy maximum rate of 5 cycles per EIA-364-09 Perform 25 plug-and-unplug of maximum rate of 5 cycles per EIA-364-28	r minute. vcles at a r minute. cycles at a	No e	per fork loc vidence of p damage	k
	(Preconditioning)	Perform 5 plug-and-unplug cy maximum rate of 5 cycles per EIA-364-09 Perform 25 plug-and-unplug of maximum rate of 5 cycles per EIA-364-28	minute.		damage	hysical
9	Durability	Perform 25 plug-and-unplug of maximum rate of 5 cycles per EIA-364-28		No e		
			minute.	No evidence of physical damage		
10	Vibration	Mated connectors Random profile: 5 Hz @ 0.01 g^2 /Hz to 20 Hz @ (slope up) 20 Hz to 500 Hz @ 0.02 g^2 /H Input acceleration is 3.13 g R 10 minutes per axis for all 3 a samples Random control limit toleranc Module weight 40 ± 2g with co 18-20 mm from module mating	z (flat) MS xes on all e is ± 3 dB enter of gravity		vidence of p damage iscontinuitie microsecor	s of ≥ 1
11	Shock (Mechanical)	Mated Connectors. Profile: Trapezoidal shock 50 Duration : 11 millisecond Velocity change : 170 " per se Quantity: Three drops in each directions. Total 18 drops per Module weight 40 ± 2 g with 1 height with center of gravity 8 module mating edge.	cond, ±10% of six connector 8.75mm card	No evidence of physical damage No discontinuities of ≥ 1 microsecond No evidence of physical damage		s of ≥ 1
12	Reseating	EIA-364-09 Manually mate and unmate th with the module card for 3 cyc maximum rate of 5 cycles per	cles at a			hysical
13	Latch Overstress Force	Unmated connectors: apply a force on the latch at a rate of per minute in the fully open per	of 25.4 ± 6mm			
14	Latch Actuation Force	rate of 25.4 ± 6mm per minute	The force to fully actuat ate of 25.4 ± 6 mm per minute with the test lade of 1.50 ± 0.01 mm (GS-010-1) inserted MAXIMUM per latch			e 3.5 kgf
15	Module Rip-out	Pull up from the center of the	test module	9.	.1kgf MININ	IUM
SION: E	CR/ECN INFORMATIO	<u>N:</u> <u>TITLE:</u> PRODU	CT SPECIF	ICATIO	ON	SHEET N
2	EC No: S2015-0772	DDR4 D	IMM, 0.85m	m PIT	СН	3 of 7
<u> </u>	<u>DATE:</u> 2014/11/20		RU-HOLE,			3 01 1
UMENT	NUMBER:	CREATED / REVISED BY:	CHECKED			OVED BY:
PS-1	51080-0001	CC TEH	CG TAN	N	SH	LENI



PRODUCT SPECIFICATION

5.2 MECHANICAL REQUIREMENTS

ITEM	DESCRIPTION	TEST CONDITION	REQUIREMENT
	Force	1.50 \pm 0.01 mm thick (GS-010-1) at a rate of 25.4 \pm 6 mm per minute. with the latches closed.	Retention force of the module in the connector with no damage
16	Insertion Force – Connector to board	EIA-364-05 Unmated connectors: Push connector into applicable PCB at a rate of 12.7 ± 3 mm per minute. Fork lock PCB hole size: 2.45 ± 0.05 mm	75 N MAXIMUM
17	Retention Force – Connector to board	EIA-364-05 Unmated connectors: Pull or push with a force of 0.45 kgf the connector mounted on the PCB at a rate of 12.7 ± 3 mm per minute. Fork lock PCB hole size: 2.45 ± 0.05 mm	No lifting of connector from applicable PCB
18	Unmating Force (perpinpair)	EIA-364-13 – Method B Pull out 1.30 ± 0.01 mm thick (GS-010-2) test blade from connector with latches removed at a rate of 12.7 ± 3mm per minute.	2.02 kgf MINIMUM for 288 circuits (14 gf per pin pair)

5.3 ENVIRONMENTAL REQUIREMENTS

ITEM	DESCRIPTION	TEST CO	NDITION	F	REQUIREME	NT	
		Duration A-4	IA-364-32 – Method A, Test Condition I, Duration A-4 Nate connectors: expose to 10 cycles of				
	Shock (Thermal)	Temperature (°C) Duration (min)		n)			
19		-55^{+0}_{-3}	30		None		
		Specimen transfer from cold to hot	5 MAXIMUN	М			
		85^{+3}_{-0}	30				
		Specimen transfer from cold to hot	5 MAXIMUN	М			
		EIA 365-17 – Method A	(without electri	cal			
20	Temperature Life (Preconditioning)	load) Mate connector: expose 72 hours at 105 ° ± 2 °C.			None		
		Exposure time as per E	able 9				
21	Temperature Life	load)	5-17 – Method A (without electrical onnector: expose 120 hours at 105 ° ± None				
		Exposure time as per E	EIA-364-1000, T	able 8	à		
22	Solderability	Unmated connector: Steam age for 8 hours Dip solder tails into sole	team age for 8 hours \pm 15 minute.95% MINIMUvip solder tails into solder pot at aSolder coverageemperature of 245 \pm 5°C for 5 \pm 0.5Solder coverage				
SION:	ECR/ECN INFORMATIO	N: TITLE: PRO	ODUCT SP	FCIFICATI	ON	SHEET N	
	<u>EC No:</u> S2015-0772		DDR4 DIMM, 0.85m				
2	<u>DATE:</u> 2014/11/20		°, THRU-HC			4 of 7	
UMENT	NUMBER:	CREATED / REVISE	<u>DBY:</u> <u>CHI</u>	ECKED BY:	APPRO	OVED BY:	
	151080-0001	CC TEH		G TAN	ец	LENI	



EC No: **S2015-0772**

PRODUCT SPECIFICATION

5.3 ENVIRONMENTAL REQUIREMENTS

ITEM	DESCRIPTION	TEST CO	NDITION	REQUIREMENT
		Rate: 25.4 ± 6 mm per s Flux type: ROL0	second	
23	Resistance to Solder Heat	E-364-56 Dip solder tails into solo for 5 ± 1 seconds. Immerse leads to a dep from connector body.	·	Visual: No damage or discoloration c connector materials.
24	Temperature Rise	EIA-364-70 Ten pair contacts in cor on the same side of the connected in a series c condition). A thermocor through holes in the so close to the contact inte Supply the rated curren	e connector are ircuit (mated uple is inserted cket housing, as erface as possible.	Maximum Temperature Rise shall no exceed 30 °C above ambient
25	Cyclic Temperature & Humidity	EIA-364-1000 – Test G temperature and humid Cycle the connector be Cycle between tempe Temperature (°C) 25 \pm 3 65 \pm 3 Ramp times should be times should be 1 hour when the temperature a stabilized within the spe 24 such cycles	lity tween rature and RH RH (%) 80 ± 3 50 ± 3 0.5 hour and dwell . Dwell times start and humidity have	None
26	Mixed Flowing Gas	EIA-364-65 – Class IIA Exposure time: EIA-364 Expose unmated conne MFG chamber. Expose module mated during te preconditioning) conne MFG chamber.	4-1000 – Table 4.1. ector for 112 hours in mated (to same test emp life	None
27	Thermal Disturbance	EIA-364-1000 – Table 4 Mated connector. Cycle the connector be $85 \pm 3^{\circ}$ C, as measured should be a minimum of Dwell times should ens reach temperature extra of 5 minutes). No humid 10 cycles.	tween $15 \pm 3^{\circ}$ C and on the part. Ramps of 2° C per minute. ure that contacts eme (for a minimum	None

5 of 7

DDR4 DIMM, 0.85mm PITCH 2 25°, THRU-HOLE, 288CKT DATE: 2014/11/20 DOCUMENT NUMBER: CREATED / REVISED BY: CHECKED BY: APPROVED BY: PS-151080-0001 CC TEH CG TAN SH LENI TEMPLATE FILENAME: PRODUCT_SPEC[SIZE_A4](V.1).DOC

molex[®]

PRODUCT SPECIFICATION

6.0 TEST SEQUENCE

SEQUENCE												
	1	2	3	4	5	6	7	8	9	10	11	12
Low level contact resistance (Initial)	1	1	1		1							1
Durability (Preconditioning)	2	2	2									2
Durability					2							
Low level contact resistance (Change from initial)	4,6	4,6,8	3,5,7		3							4,6,8,10,12
Insulation resistance				1, 5								
Dielectric withstanding voltage				2,6								
Temperature life (Preconditioning)												3
Temperature life	3											
Shock (Thermal)		3		3								
Thermal disturbance												9
Cyclic temperature & humidity		5		4								
Mixed flowing gas (Unmated)												5
Mixed flowing gas (Mated)												7
Shock (Mechanical)			6									
Vibration			4									
Reseating	5	7										11
Temperature rise						1						
Solderability							1					
Resistance to solder heat								3				
nsertion Force (Module to connector with latches)									1			
Latch Actuation Force										1		
Latch Overstress Force										2		
Module Rip-out Force									2			
Insertion force – Connector to board											1	
Retention force – Connector to Board											2	
Retention Force - Terminal								1,4				
Retention Force – Fork lock								2,5				
Unmating Force (per pin pair)											3	
Sample Size per Test Group	5	5	5	5	5	5	5	6	5	5	5	5

molex

PRODUCT SPECIFICATION

7.0 PACKAGING

Parts shall be packed in trays and protected against damage during handling, transportation and storage.

8.0 RECOMMENDED LEAD-FREE REFLOW PROFILES

Connector should be soldered onto PCB using either the wave soldering technique or the reflow soldering technique according to the table shown below.

Process	Peak Temperature	Duration
Wave	265°C MAXIMUM (Solder Batch) 220°C MAXIMUM Connector housing	5 ± 2 second (wave contact)
Reflow	220°C MAXIMUM Peak 220°C MAXIMUM Connector housing	20 to 40 seconds Time within 5 °C of peak

REVISION:	ECR/ECN INFORMATION:	TITLE: PRODU	- FRODUCT SPECIFICATION				
2	<u>EC No:</u> S2015-0772	DDR4 DIMM, 0.85mm PITCH			7 of 7		
<u>DATE:</u> 2014/11/20 25°, THRU-HOLE, 288C							
DOCUMENT NUMBER:		CREATED / REVISED BY:	CHECKED BY:	APPROVED BY:			
PS-151080-0001		CC TEH	CG TAN	CG TAN SH I			
TEMPI ATE FIL ENAME: PRODUCT. SPECISIZ					CISIZE A41(V 1) DOC		