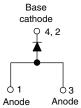


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# High Performance Schottky Rectifier, 3.0 A





D-PAK ("	ΓO-252AA)
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Base	е				
catho	de				
01 03					
Anode	Anode				

PRODUCT SUMMARY					
Package	D-PAK (TO-252AA)				
I <sub>F(AV)</sub>	3.0 A				
V <sub>R</sub>	20 V, 30 V, 40 V				
V <sub>F</sub> at I <sub>F</sub>	0.49 V				
I <sub>RM</sub>	20 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Single die				
E <sub>AS</sub>	8 mJ				

#### **FEATURES**

- Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability



**FREE** 

- Popular D-PAK outline
- · Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION**

The VS-MBRD320-M3, VS-MBRD330-M3, VS-MBRD340-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES UNITS							
I <sub>F(AV)</sub>	Rectangular waveform	3.0	А				
V <sub>RRM</sub>		20 to 40	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	490	Α				
V <sub>F</sub>	3 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.49	V				
TJ		-40 to +150	°C				

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-MBRD320-M3	VS-MBRD330-M3	VS-MBRD340-M3	UNITS		
Maximum DC reverse voltage	$V_{R}$	20	30	40	V		
Maximum working peak reverse voltage	$V_{RWM}$	20	30	40	V		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS		
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>L</sub> = 133 °C, re	50 % duty cycle at T <sub>L</sub> = 133 °C, rectangular waveform				
Maximum peak one cycle non-repetitive surge current	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	490	A		
		10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	75			
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 16 mH		8.0	mJ		
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		1.0	А		



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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	ONDITIONS	TYP.	MAX.	UNITS	
		3 A	T <sub>.1</sub> = 25 °C	0.48	0.6		
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	6 A	1]=25 0	0.58	0.7	V	
See fig. 1	VFM ('')	3 A	T <sub>.1</sub> = 125 °C	0.41	0.49		
		6 A	1j = 125 C	0.55	0.625		
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.02	0.2	mA	
See fig. 2	IRM **/	T <sub>J</sub> = 125 °C		10.7	20	IIIA	
Typical junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		189	-	pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		5.0	-	nΗ	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		-	10 000	V/µs	

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T <sub>J</sub> <sup>(1)</sup>		-40 to +150	20
Maximum storage temperature range	T <sub>Stg</sub>		-40 to +175	°C
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation See fig. 4	6.0	°C AM
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>		80	°C/W
Annyayimata waight			0.3	g
Approximate weight			0.01	OZ.
			MBRI	D320
Marking device		Case style D-PAK (similar to TO-252AA)	MBRD330	
			MBRI	D340

#### Note

(1) 
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink

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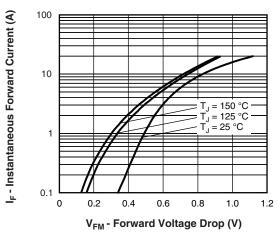


Fig. 1 - Maximum Forward Voltage Drop Characteristics

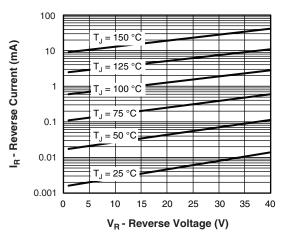


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

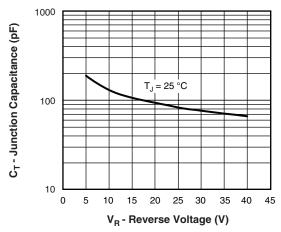


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

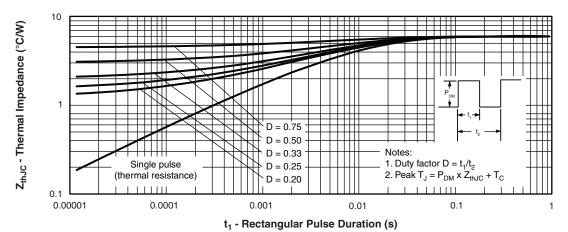


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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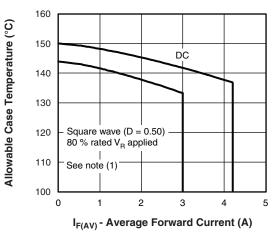


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

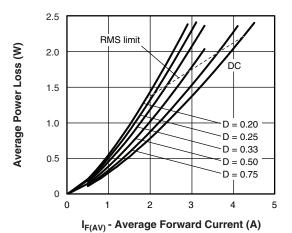


Fig. 6 - Forward Power Loss Characteristics

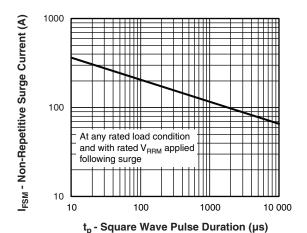


Fig. 7 - Maximum Non-Repetitive Surge Current

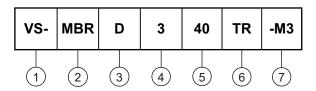
### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$ 

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### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Schottky MBR series

- D = TO-252AA (D-PAK)

- Current rating (3 = 3 A)
- Voltage ratings

20 = 20 V 30 = 30 V40 = 40 V

6 - • None = tube

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

7 - Environmental digit:

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-MBRD320-M3	75	3000	Antistatic plastic tube				
VS-MBRD320TR-M3	2000	2000	13" diameter reel				
VS-MBRD320TRL-M3	3000	3000	13" diameter reel				
VS-MBRD320TRR-M3	3000	3000	13" diameter reel				
VS-MBRD330-M3	75	3000	Antistatic plastic tube				
VS-MBRD330TR-M3	2000	2000	13" diameter reel				
VS-MBRD330TRL-M3	3000	3000	13" diameter reel				
VS-MBRD330TRR-M3	3000	3000	13" diameter reel				
VS-MBRD340-M3	75	3000	Antistatic plastic tube				
VS-MBRD340TR-M3	2000	2000	13" diameter reel				
VS-MBRD340TRL-M3	3000	3000	13" diameter reel				
VS-MBRD340TRR-M3	3000	3000	13" diameter reel				

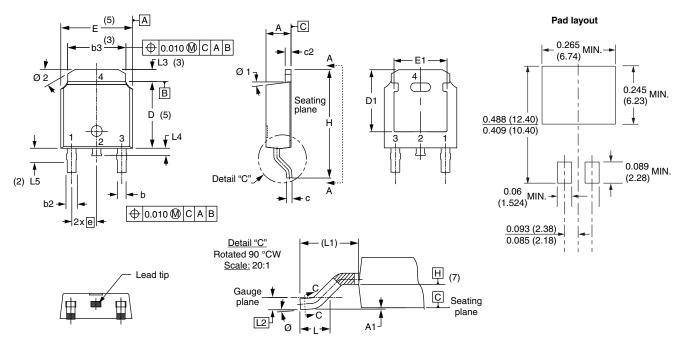
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95627					
Part marking information	www.vishay.com/doc?95176				
Packaging information	www.vishay.com/doc?95033				



### Vishay Semiconductors

# D-PAK (TO-252AA) "M"

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	-	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	5.21	-	0.205	-	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIMETERS		INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	BSC	0.090	BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74	2.74 BSC		0.108 REF.	
L2	0.51	BSC	0.020 BSC		
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
Ø	0°	10°	0°	10°	
Ø1	0°	15°	0°	15°	
Ø2	25°	35°	25°	35°	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- (5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (6) Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC® outline TO-252AA



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