

Features

- Trench Power LV MOSFET Technology
- Excellent Package For Heat Dissipation
- Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient^(Note1)
- Thermal Resistance: 2.5°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current T _C =25°C	I _D	50	A
T _C =100°C		32	
Pulsed Drain Current ^(Note2)	I _{DM}	200	A
Total Power Dissipation ^(Note3)	P _D	50	W
Single Pulse Avalanche Energy ^(Note 4)	E _{AS}	53	mJ

Note:

1. The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz.

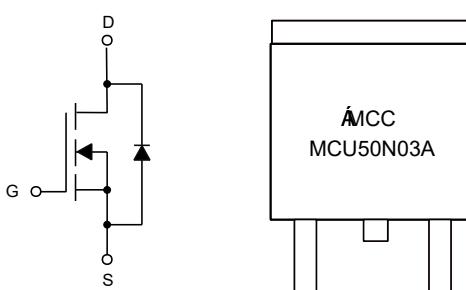
Copper, in a still air environment with T_A=25°C. The Power dissipation P_{DSM} is based on R_{θJA} t≤ 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.

2. Repetitive rating; pulse width limited by max. junction temperature.

3. P_D is based on max. junction temperature, using junction-case thermal resistance.

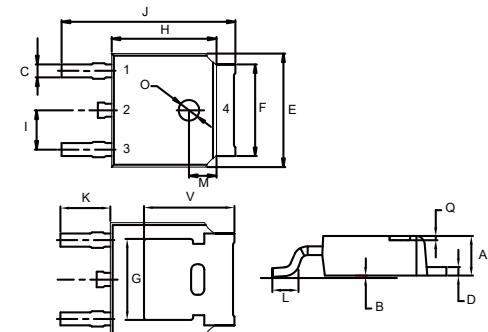
4. T_J=25°C, V_{DD}=25V, V_{GS}=10V, L=0.5mH.

Internal Structure and Marking Code



N-CHANNEL MOSFET

DPAK(TO-252)



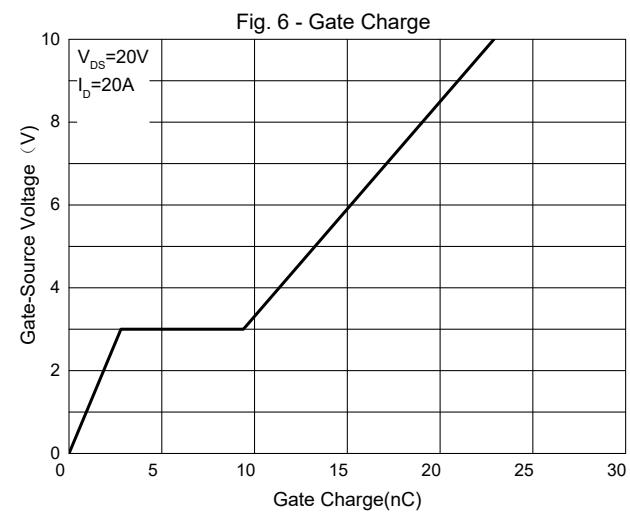
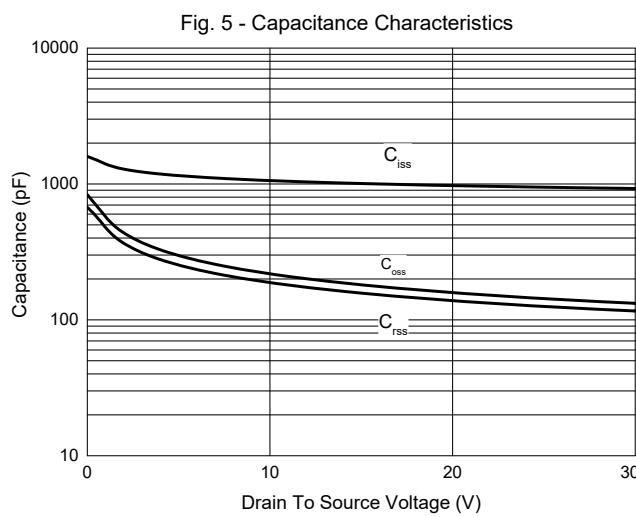
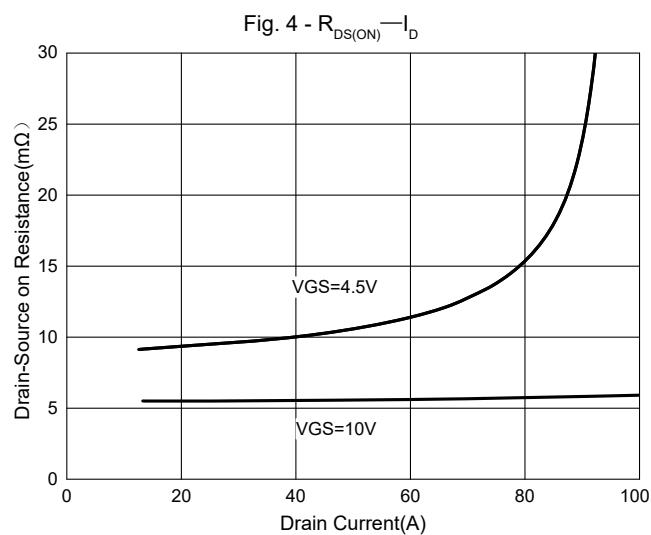
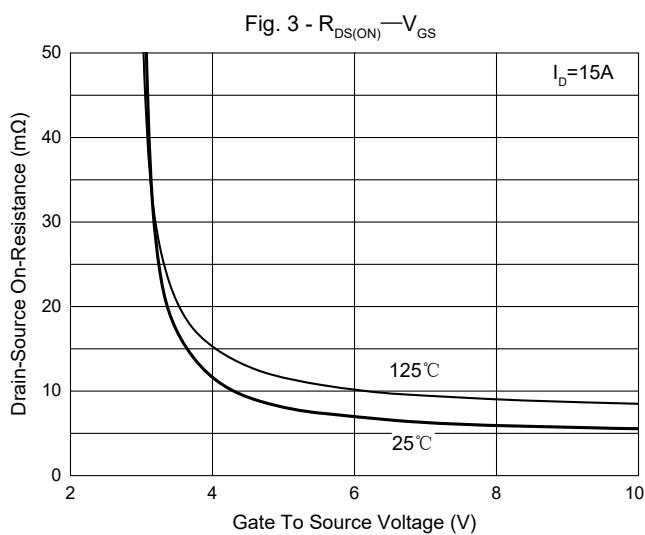
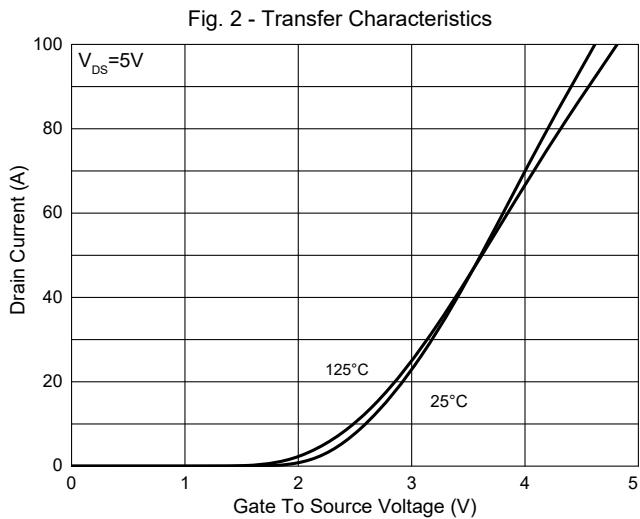
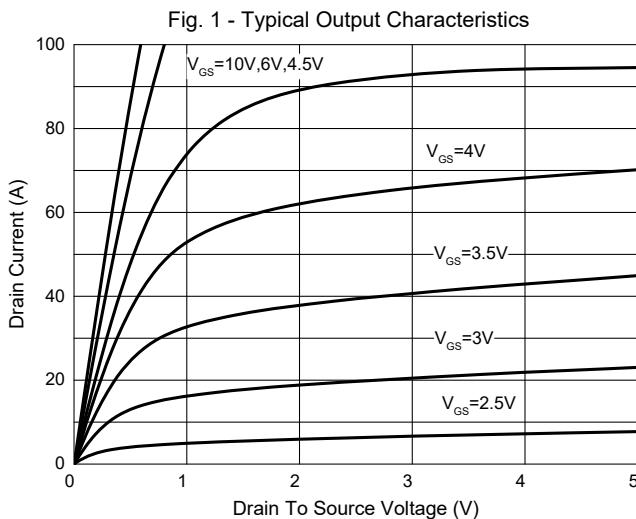
1. Gate
- 2,4. Drain
3. Source

DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.087	0.094	2.20	2.40	
B	0.000	0.005	0.00	0.13	
C	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
E	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
H	0.236	0.244	6.00	6.20	
I	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.114		2.90		TYP.
L	0.055	0.067	1.40	1.70	
M	0.063		1.60		TYP.
O	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.35		TYP.

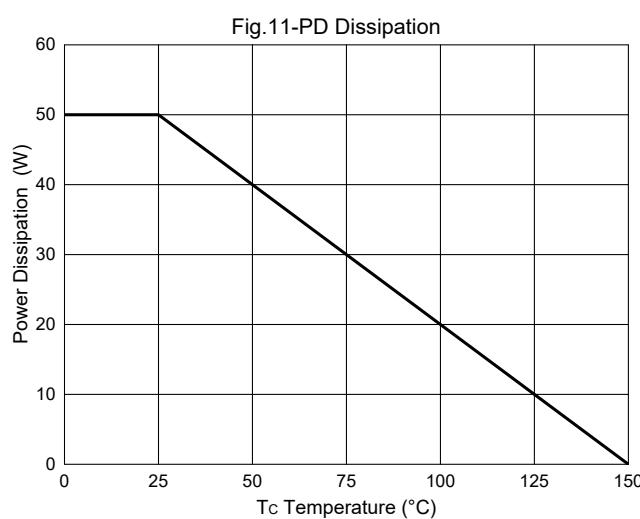
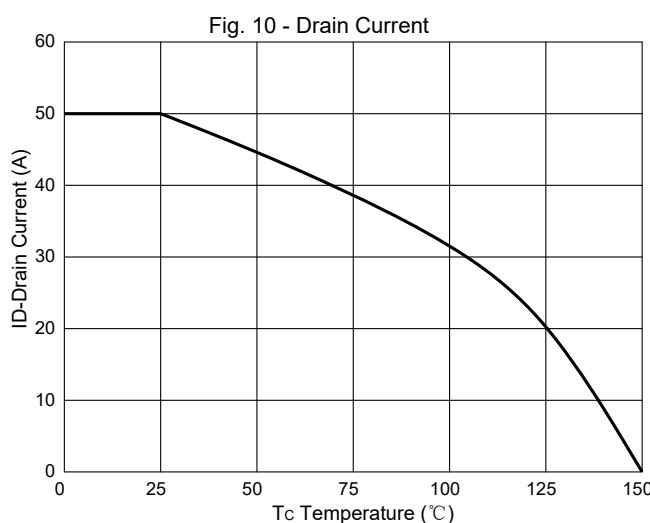
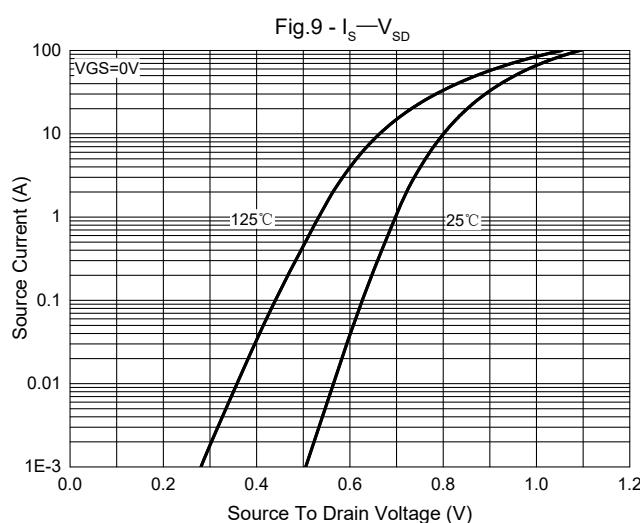
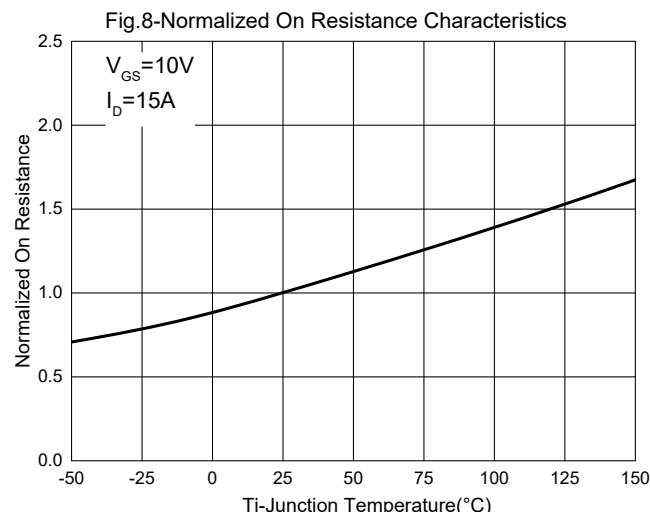
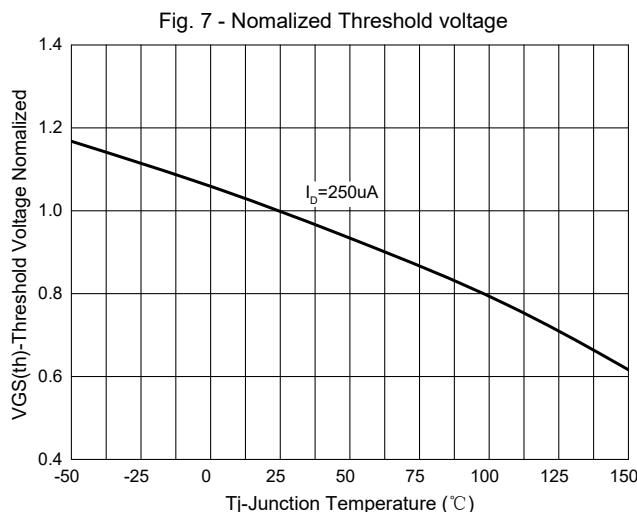
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	30			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$			1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.5	2.2	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=15A$		5.4	7.3	$m\Omega$
		$V_{GS}=4.5V, I_D=15A$		9.5	11.5	$m\Omega$
Gate Resistance	R_g	F=1 MHz, Open drain		2.2		Ω
Diode Characteristics						
Continuous Body Diode Current	I_S				50	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=15A$			1.2	V
Reverse Recovery Time	t_{rr}	$I_S=20A, dI_F/dt=100A/\mu s$		18		ns
Reverse Recovery Charge	Q_{rr}			7		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=15V, V_{GS}=0V, f=1MHz$		1007		pF
Output Capacitance	C_{oss}			181		
Reverse Transfer Capacitance	C_{rss}			157		
Total Gate Charge	Q_g	$V_{DS}=20V, V_{GS}=10V, I_D=20A$		22.9		nC
Gate-Source Charge	Q_{gs}			2.8		
Gate-Drain Charge	Q_{gd}			6.6		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10V, V_{DD}=20V, I_D=2A$ $R_{GEN}=3\Omega$		7		ns
Turn-On Rise Time	t_r			9		
Turn-Off Delay Time	$t_{d(off)}$			24		
Turn-Off Fall Time	t_f			9.4		

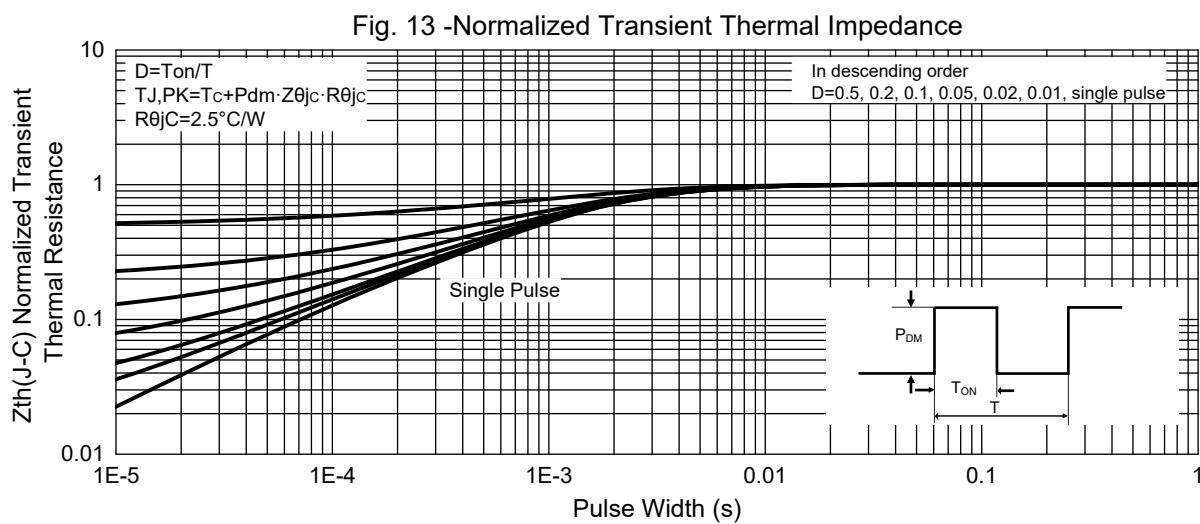
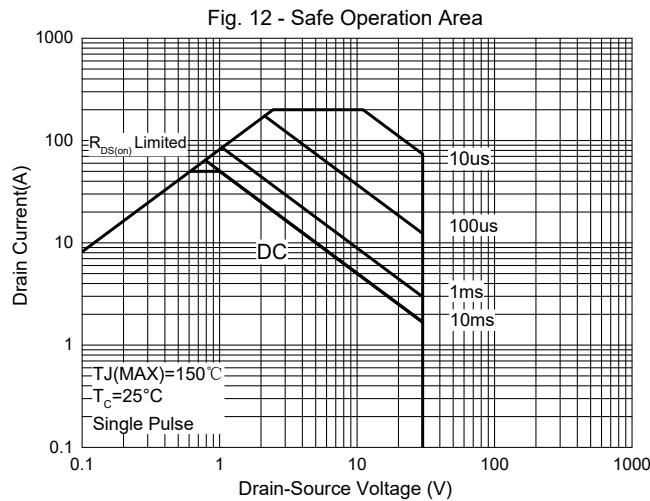
Curve Characteristics



Curve Characteristics



Curve Characteristics



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel

Note : Adding "-HF" Suffix for Halogen Free, eg. Part Number-TP-HF

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