

N-Channel Enhancement Mode Power MOSFET

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|---|--|
| <p>Description</p> <p>The 03N06 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.</p> <p>General Features</p> <ul style="list-style-type: none"> ● V_{DS} 60V ● I_D (at $V_{GS} = 10V$) 3A ● $R_{DS(ON)}$ (at $V_{GS} = 10V$) < 80mΩ ● $R_{DS(ON)}$ (at $V_{GS} = 4.5V$) < 90mΩ ● 100% Avalanche Tested ● RoHS Compliant <p>Application</p> <ul style="list-style-type: none"> ● Power switch ● DC/DC converters | <p>Schematic diagram</p> <p>SOT-23</p> |
|---|--|

Ordering Information

| Device | Package | Marking | Packaging |
|--------|---------|---------|--------------|
| 03N06 | SOT-23 | 03N06 | 3000pcs/Reel |

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted

| Parameter | Symbol | Value | Unit |
|--|----------------|------------|------------------|
| Drain-Source Voltage | V_{DS} | 60 | V |
| Continuous Drain Current | I_D | 3 | A |
| Pulsed Drain Current (note1) | I_{DM} | 12 | A |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Power Dissipation | P_D | 1.7 | W |
| Operating Junction and Storage Temperature Range | T_J, T_{stg} | -55 To 150 | $^\circ\text{C}$ |

Thermal Resistance

| Parameter | Symbol | Value | Unit |
|--------------------------|------------|-------|--------------------|
| Maximum Junction-to-Case | R_{thJC} | 73.5 | $^\circ\text{C/W}$ |

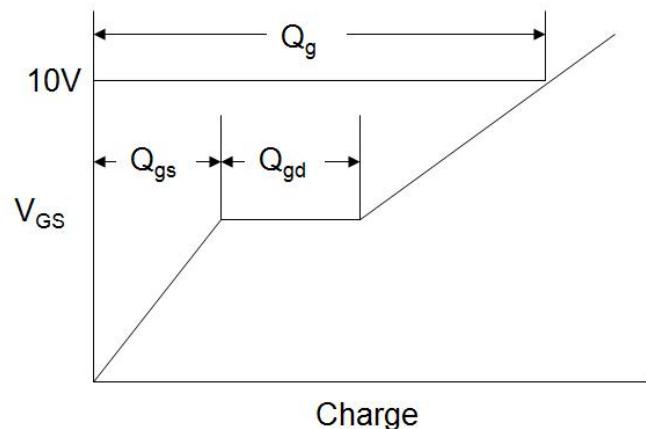
Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

| Parameter | Symbol | Test Conditions | Value | | | Unit |
|--|-----------------------------|--|-------|------|-----------|------------------|
| | | | Min. | Typ. | Max. | |
| Static Parameters | | | | | | |
| Drain-Source Breakdown Voltage | $V_{(\text{BR})\text{DSS}}$ | $V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$ | 60 | -- | -- | V |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{\text{DS}} = 60\text{V}, V_{\text{GS}} = 0\text{V}$ | -- | -- | 1 | μA |
| Gate-Source Leakage | I_{GSS} | $V_{\text{GS}} = \pm 20\text{V}$ | -- | -- | ± 100 | nA |
| Gate-Source Threshold Voltage | $V_{\text{GS}(\text{th})}$ | $V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$ | 0.7 | 1.0 | 1.2 | V |
| Drain-Source On-Resistance | $R_{\text{DS}(\text{on})}$ | $V_{\text{GS}} = 10\text{V}, I_D = 2\text{A}$ | -- | 65 | 80 | $\text{m}\Omega$ |
| | | $V_{\text{GS}} = 4.5\text{V}, I_D = 1\text{A}$ | -- | 70 | 90 | |
| Forward Transconductance | g_{FS} | $V_{\text{GS}} = 5\text{V}, I_D = 2\text{A}$ | -- | 9 | -- | S |
| Dynamic Parameters | | | | | | |
| Input Capacitance | C_{iss} | $V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 30\text{V}, f = 1.0\text{MHz}$ | -- | 458 | -- | pF |
| Output Capacitance | C_{oss} | | -- | 24 | -- | |
| Reverse Transfer Capacitance | C_{rss} | | -- | 21 | -- | |
| Total Gate Charge | Q_g | $V_{\text{DD}} = 30\text{V}, I_D = 2\text{A}, V_{\text{GS}} = 10\text{V}$ | -- | 14.6 | -- | nC |
| Gate-Source Charge | Q_{gs} | | -- | 1.6 | -- | |
| Gate-Drain Charge | Q_{gd} | | -- | 3 | -- | |
| Turn-on Delay Time | $t_{\text{d}(\text{on})}$ | $V_{\text{DD}} = 30\text{V}, I_D = 2\text{A}, R_G = 1\Omega$ | -- | 6 | -- | ns |
| Turn-on Rise Time | t_r | | -- | 15 | -- | |
| Turn-off Delay Time | $t_{\text{d}(\text{off})}$ | | -- | 15 | -- | |
| Turn-off Fall Time | t_f | | -- | 10 | -- | |
| Drain-Source Body Diode Characteristics | | | | | | |
| Continuous Body Diode Current | I_S | $T_C = 25^\circ\text{C}$ | -- | -- | 3 | A |
| Body Diode Voltage | V_{SD} | $T_J = 25^\circ\text{C}, I_{\text{SD}} = 2\text{A}, V_{\text{GS}} = 0\text{V}$ | -- | -- | 1.2 | V |
| Reverse Recovery Charge | Q_{rr} | $I_F = 2\text{A}, V_{\text{GS}} = 0\text{V}$ $dI/dt = 100\text{A/us}$ | -- | 38 | -- | nC |
| Reverse Recovery Time | T_{rr} | | -- | 36 | -- | ns |

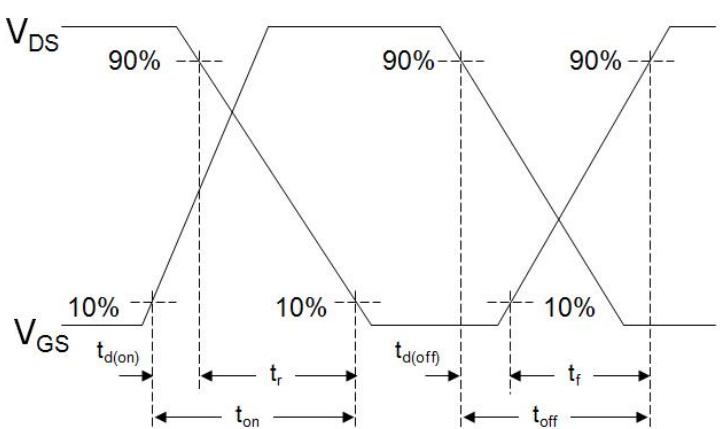
Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Identical low side and high side switch with identical R_G

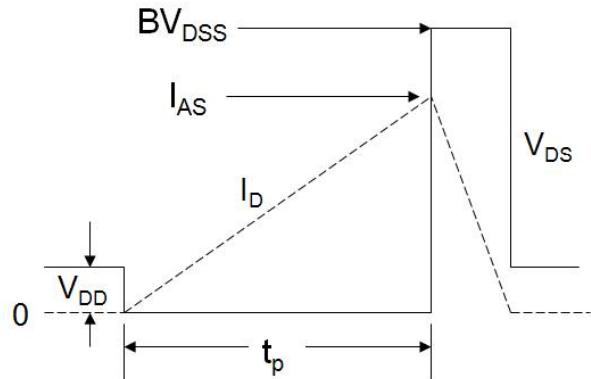
Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

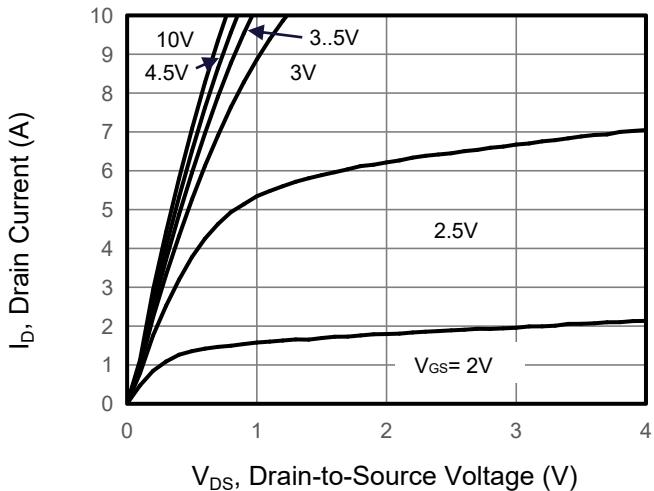


Figure 2. Transfer Characteristics

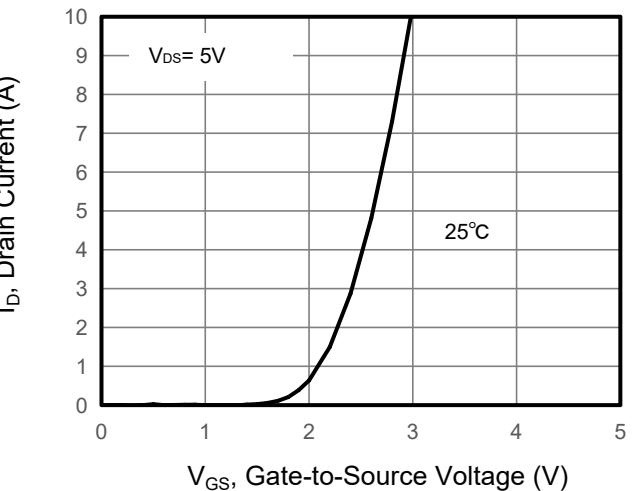


Figure 3. Drain Source On Resistance

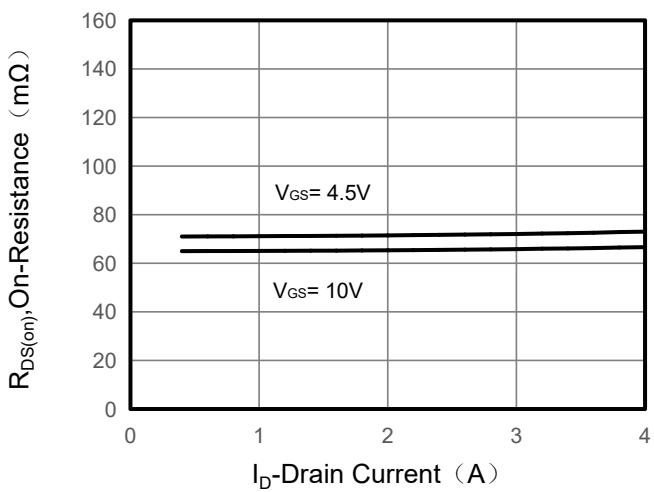


Figure 4. Gate Charge

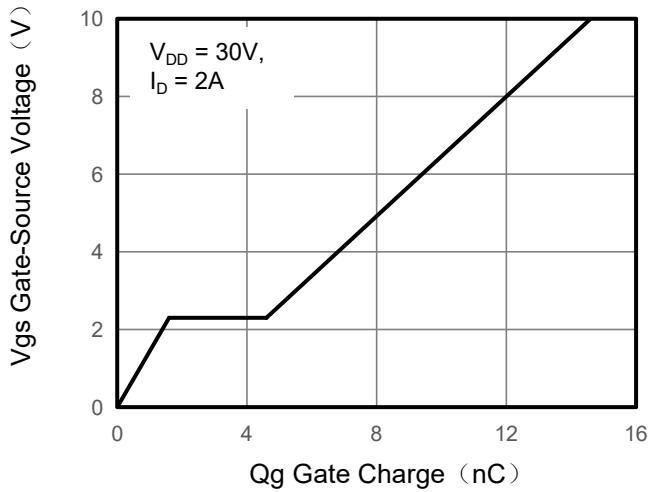


Figure 5. Capacitance

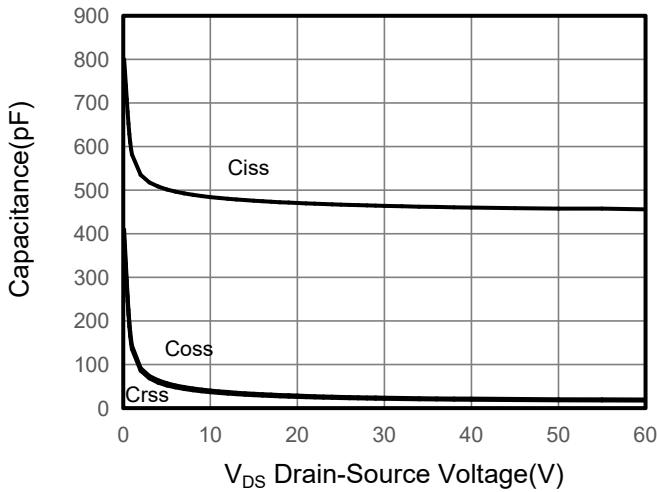
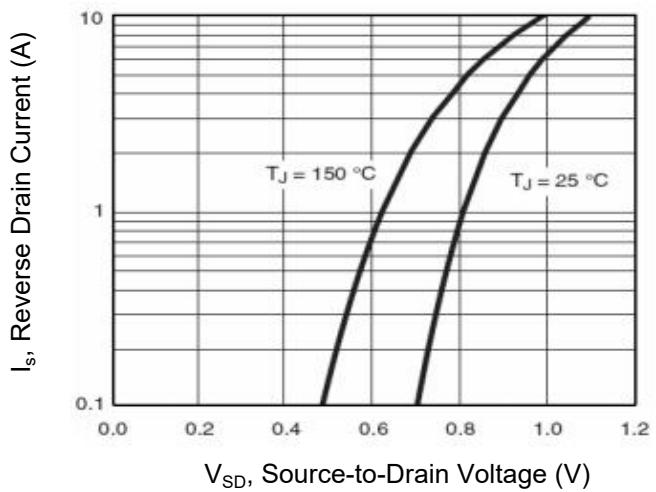


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

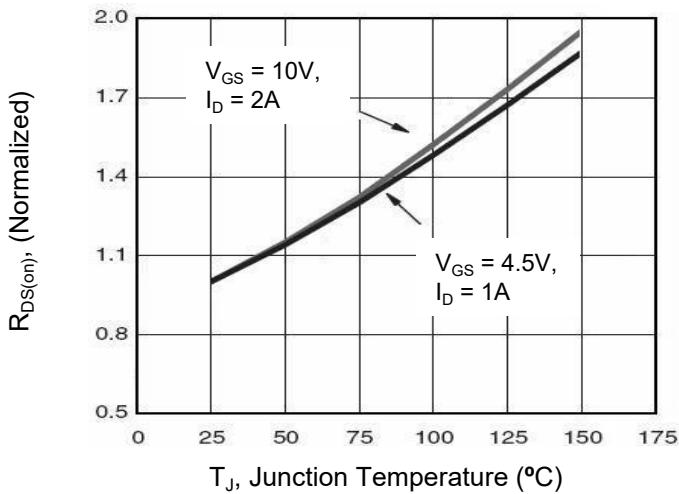


Figure 8. Safe Operation Area

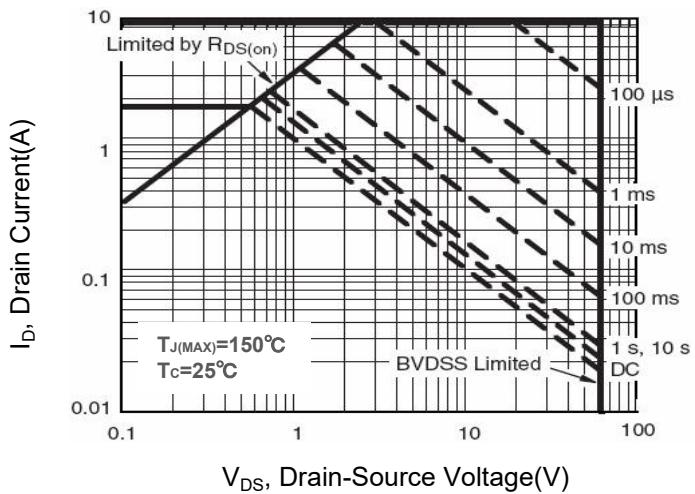
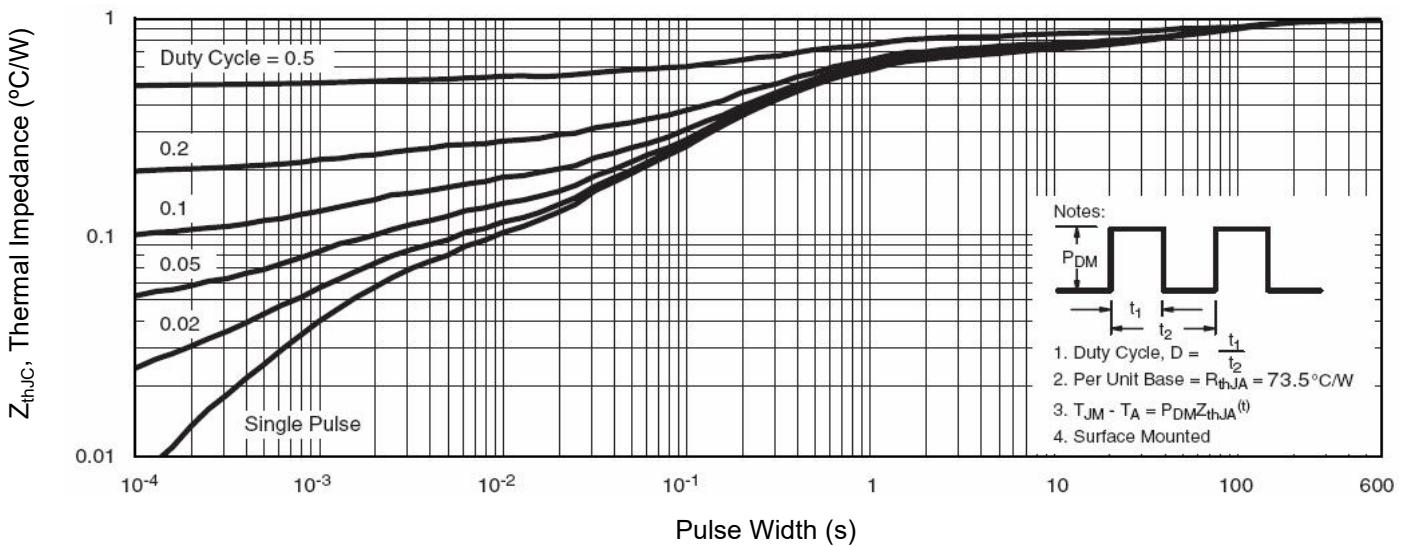
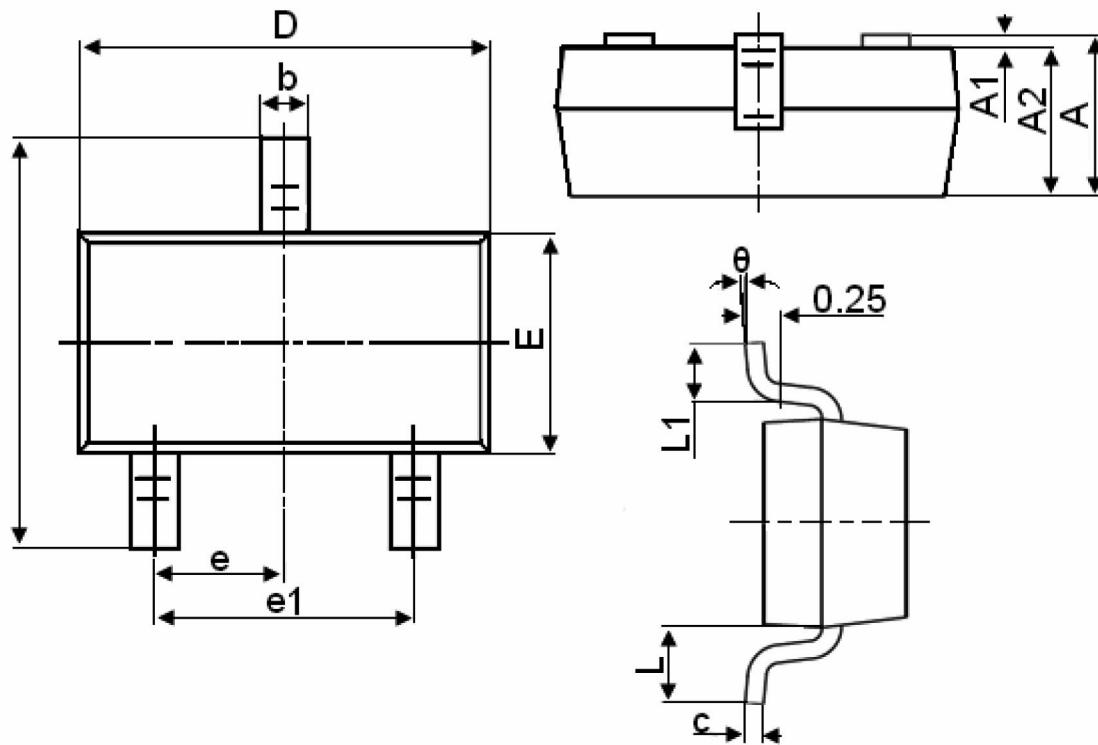


Figure 9. Normalized Maximum Transient Thermal Impedance



SOT-23 Package Information

| Symbol | Dimensions in Millimeters | |
|--------|---------------------------|-------|
| | MIN. | MAX. |
| A | 0.900 | 1.150 |
| A1 | 0.000 | 0.100 |
| A2 | 0.900 | 1.050 |
| b | 0.300 | 0.500 |
| c | 0.080 | 0.150 |
| D | 2.800 | 3.000 |
| E | 1.200 | 1.400 |
| E1 | 2.250 | 2.550 |
| e | 0.950TYP | |
| e1 | 1.800 | 2.000 |
| L | 0.550REF | |
| L1 | 0.300 | 0.500 |
| θ | 0° | 8° |