PCN Nu	mber:	201410300	01				PCN Dat	:e:	11/03/2014	
Title:	Title: TAS5424DKE new BOM									
Custom	er Contact:	PCN Manager		Phone:	+1(214)480-6037		Dept: Qu		Quality Services	
Proposed 1 <sup>st</sup> Ship Date:		05/03/2014		Estimated Sample Availability:		8 weeks after request		er request		
Change	Туре:									
Ass	embly Site			Design			Wafer Bump Site		p Site	
Ass	embly Process			Data Sheet			Wafer Bump Material		p Material	
Ass	embly Material	S		Part number change			Wafer Bump Process			
Med	chanical Specifi	cation		Test Site			Wafer Fab Site			
Packing/Shipping/Labeling			Test Process			Wafer Fab Materials		Materials		
							Wafer I	Fab I	Process	
	PCN Details									
Descrip	tion of Chang	e:								

Texas Instruments Incorporated is announcing the qualification for TAS5424xTDKERQ1 Cu wire, new L/F supplier and Mold Compound.

	From:	To:				
Lead Frame	PSMC	Shinko				
Mold Compound	Sumitomo EME-G600	Sumitomo G700LS				
Bond Wire	Au	Cu				
Reason for Change:						

Continuity of supply.

- 1) Shinko ISO/TS leadframe supplier
- 2) To align with world technology trends and use wiring with enhanced mechanical and electrical properties.

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

Improved delamination performance with G700LS.

Changes to product identification resulting from this PCN:

No change to the product identification of the actual device or shipping labels.

**Product Affected:** 

TAS5424BTDKERQ1

TAS5424CTDKERQ1



# **Qualification Plan:**

## Automotive New Product Qualification Plan

(As per AEC-Q100 and JEDEC Guidelines)

Supplier Name:	Texas Instruments Inc.	Supplier Wafer Fabrication Site:	Dallas, Texas, USA (TI DMOS5)
Supplier Code:		Supplier Die Rev:	D2
Supplier Part Number:	TAS5424BTDKEQ1 TAS5424CTDKERQ1	Supplier Assembly/Test Site:	Assembly: Amkor Philippines (AP1) Test: TI Taiwan, Taipei ,Taiwan (TAI)
Customer Name:		Supplier Package/Pin:	DKE / 44
Customer Part Number:	324836-0040	Pb Free Lead Frame (Y/N):	Y
Device Description:	FOUR-CHANNEL AUTOMOTIVE DIGITAL AMPLIFIERS	"Green" Mold Compound (Y/N):	Y
MSL Rating:	Level 3	Operating Temp Range:	TA=40°C to +105°C
Peak Solder Reflow Temp:	245°C	Automotive Grade Level (1):	Level 2
Prepared by Signature:	Anita Bills / David McCain	Date:	10/29/2014

Test	#	Reference	Test Conditions	Min Lots (2)	SS / lot (2)	Min Total (2)	Results Lot/pass/fail	Comments: (N/A =Not Applicable)	Exceptions to AEC -Q100
			TEST GROUP A – ACCELERATED ENVIR	RONMENT ST	RESS TE	STS (3)			
PC	A1	JESD22 A113 J-STD-020	Preconditioning: SMD only; Moisture Preconditioning for THB/HAST, AC/UHST, TC, HTSL, PTC	Performed devices, Pri TC , PTC, H	ior to TH			L3-245°C (Use Auto Reflow profile) (3 lots)	
THB or HAST	A2	JESD22 A101 JESD22 A110	Temperature Humidity Bias: 85°C/85%/1000 hours Highly Accelerated Stress Test: 130°C/85% RH/96 hours	3	77	231			
AC or UHST or TH	A3	JESD22 A102 or JESD22 A118 or JESD22 A101	Autoclave: 121°C/2 atm/96 hours Unbiased Highly Accelerated Stress Test 130°C/85% RH/96 hours Temperature-Humidity (without bias) 85°C/85% RH/1000 hours	3	77	231			
TC	A4	JESD22 A104 and Appendix 3	Temperature Cycle: -65°C/+150°C/500 cycles Post Temp Cycle Bond Pull	3	77	231			
PTC	A5	JESD22-A105	3 grams minimum (30 bonds total) Power Temperature Cycle:	1	45	45			
HTSL	A6	JESD22 A103	-40°C to +105°C for 1000 cycles High Temperature Storage Life: 150°C/1000 hours	1	45	45			
			TEST GROUP B – ACCELERATED LIFET	IME SIMULA	TION TES	STS (3)			
HTOL	B1	JESD22 A108	High Temp Operating Life: 125°C/1000 hours/V <sub>DD</sub> Max	3	77	231			
ELFR	B2	AEC-Q100-008	Early Life Failure Rate: 125°C/24 hours/V <sub>DD</sub> Max	3	800	2400			
EDR	B3	AEC Q100-005	NVM Endurance, Data Retention, and Operational Life	3	77	231		N/A	
			TEST GROUP C – PACKAGE ASSEM			(3)			
WBS	C1	AEC-Q100-001	Wire Bond Shear Test: (Ppk > 1.67 and Cpk > 1.33)	30 bonds	5 parts	30 wires			

1105	CI	ALC QIOU UUI		50 501103	5	50		
					parts	wires		
					min.			
WBP	C2	Mil-Std-883	Wire Bond Pull: Each bonder used. (Ppk > 1.67 and	30 bonds	5	30		
		Method 2011	Cpk > 1.33)		parts	wires		
					min.			
SD	C3	JESD22 B102	Solderability: (>95% coverage)	1	60	60		
			8 hr steam age					
PD	C4	JESD22 B100,	Physical Dimensions: (Ppk > 1.67 and Cpk > 1.33)	3	10	30		
		JESD22 B108						
SBS	C5	AEC-Q100-010	Solder Ball Shear: (Ppk > 1.67 and Cpk > 1.33)	50 balls	3	50	 N/A to non-	

							solder ball surface mount devices	
LI	C6	JESD22 B105	Lead Integrity: (No lead cracking or breaking)	50 leads	1	50	 Not Required	
		Not Required for SMT parts					for SMT parts	

#### TEST GROUP D – DIE FABRICATION RELIABILITY TESTS

EM	D1	JESD61	Electromigration:	ctromigration:				
			(Only if de-rating required beyond design rules)					
TDDB	D2	JESD35	Time Dependent Dielectric Breakdown	-	-	-	 N/A	
HCI	D3	JESD60 & 28	Hot Injection Carrier	-	-	-	 N/A	

#### **TEST GROUP E- ELECTRICAL VERIFICATION TESTS**

TEST	E1	User/Supplier Specification	Pre and Post Stress Electrical Test.	All	All	All	100% of qualification devices	
HBM	E2	AEC-Q100-002	Electrostatic Discharge, Human Body Model	-	-	-	 N/A	
MM	E2	AEC-Q100-003	Electrostatic Discharge, Machine Model:	-	-	-	 N/A	
CDM	E3	AEC-Q100-011	Electrostatic Discharge, Charged Device Model; (750V corner leads, 500V for all other leads)	-	-	-	 N/A	
LU	E4	AEC-Q100-004	Latch-Up:	-	-	-	 N/A	
ED	E5	AEC-Q100-009	Electrical Distributions: (Test across recommended operating temperature range) (Cpk > 1.67 , Ppk > 1.67)	3	30	90	25°C, 105°C, -40°C	

(1) Grade 0 (or A):

A): -40°C to +150°C ambient operating temperature range

Grade 1 (or Q): -40°C to +125°C ambient operating temperature range

Grade 2 (or T): -40°C to +105°C ambient operating temperature range

Grade 3 (or I): -40°C to +85°C ambient operating temperature range

Grade 4 (or C): -0°C to +150°C ambient operating temperature range

(2) These are recommended minimum lot/sample sizes. Lot/sample size may be reduced depending on available data.

(3) Generic data may be used.

#### **Quality and Reliability Data Disclaimer**

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customer should provide adequate design and operating safeguards. Quality and reliability data provided by Texas Instruments is intended to be an estimate of product performance based upon history only. It does not imply that any performance levels reflected in such data can be met if the product is operated outside the conditions expressly stated in the latest published data sheet or agreed-to customer specification for a device.

Reliability data shows characteristic failure mechanisms of the specific environmental stress as documented in the industry standards for each stress condition.

### For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com