PC	N Number:	201	1807100	01	F	PCN Date:	Ju	ly :	12, 201	18		
Tit	e: Datasheet fo											
Cus	stomer Contact:	PCN Manager					Dept:		Quality Services			
Change Type:												
	Assembly Site				Design				Wafer	<sup>-</sup> Bump Site		
	Assembly Process	· · · · · · · · · · · · · · · · · · ·			Data Sheet				Wafer Bump Material			
	Assembly Materials			Part numbe	number change			Wafer Bump Process				
	Mechanical Specification			Test Site	t Site			Wafer Fab Site				
Packing/Shipping/La			ling		Test Proces	S			Wafer	Fab Materials		
									Wafer	r Fab Process		
Notification Details												
Description of Change:												
Texas Instruments Incorporated is announcing an information only notification.												
The product datasheet(s) is being updated as summarized below.												
The following change history provides further details.												
	INSTRUMENTS						SLLS	SEW6	C-NOVEN	IBER 2016-REVISED JUN		
Cł	anges from Revision B	(Augu	ust 2017) 1	to R	evision C						Page	
	Changed the VCONN n	in desc	cription in t	he l	Pin Eurotions tak						3	
	Changed the VCONN pin description in the <i>Pin Functions</i> table Deleted V <sub>RX(FRS_PD)</sub> from electrical characteristics											
	<ul> <li>Deleted V<sub>RX(FRS_PD)</sub> from electrical characteristics</li> <li>Deleted t<sub>FRSWAPRX</sub> from timing requirements section</li></ul>											
	Added NOTE: "The TUSB422 supports all PD2.0 and PD 3.0" to the USB PD BMC PHY section											
	Deleted text from the first paragraph of the Fast Role Swap secton.											
	From: Once VBUS is at VSafe0V, change To: Once VBUS is at VSafe0V, disable											
	AUTO_DISCHARGE_DISCONNECT in Power Control Register and change									17		
•	From: Once VBUS is at vSafe5V, the TCPM should then send PS_RDY to its port partner. To: Once VBUS is at											
	vSafe5V, the TCPM should update message header information and then send PS_RDY to its port partner.											
•	······································											
•	Added NOTE: "When exiting dead battery mode" to the <i>Dead Battery Mode</i> section											
•												
•	Added text: "VBUS present status may be invalid" to the Bit 2 VBUS_PRESENT description in Table 31											
	Added text: "Before attempting to transmit" to the <i>Transmit Register (address = 0x50) [reset = 0x00]</i> register											
	<ul> <li>Changed bit 0 From: FAST_ROLE_SWAP_STAT To Reserved in Figure 65 and Table 62</li> <li>Changed bit 0 From: FAST_ROLE_SWAP_MASK To Reserved in Figure 66 and Table 63</li> </ul>											
Changed bit 3 From: FASTROLE_RX_EN To Reserved in Figure 69 and Table 66												
	e datasheet number	will	be chan	ging								
De	evice Family				Chan	ge From:			Chang	e To:		
тι	JSB422				SLLS	EW6B			SLLSE	W6C		
These changes may be reviewed at the datasheet links provided.												
ine	ese changes may be	revie	ewed at	τηε	e datasheet li	inks provided	J.					
http://www.ti.com/product/TUSB422												
Reason for Change:												
To accurately reflect device characteristics.												
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):												
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.												
Changes to product identification resulting from this PCN:												
None.												
Product Affected:												
Τι	JSB422IYFPR		TUSB42	<u>2I</u> Y	FPT							

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
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