



200922853 WF200/WFM200S New Revision Parts and Datasheets Available

PRCN Issue Date: 9/22/2020

Effective Date: 12/28/2020

PCN Type: Product Revision

Description of Change

Silicon Labs is pleased to announce a new revision of WF200 and WFM200S parts and release of datasheet version 1.1 and 1.0, respectively.

SOFTWARE IMPACT DESCRIPTION:

The new revisions are supported by WF Firmware version 3.4.0 or later.

Datasheet updates as follows:

WF200 datasheet - Revision 1.1

- Updated Ordering Guide
- Updated Table 9.1 Top Marking for WF200 on page 32

WFM200S datasheet - Revision 1.0

- Updated Table 2.1 WFM200S Ordering Information (R indicates Full Reel) on page 5
- Updated Table 9.1 Top Marking Definition on page 31
- Updated Section 4.4 RF Transmitter General Characteristics and Section 4.4.1 RF Transmitter Characteristics
- Updated Section 8. Land Pattern
- Updated Table 4.1 Absolute Maximum Ratings on page 8
- Added Section 12.5 Japan and Section 12.6 South Korea - KC
- Added reference to AN1048 in 12. Certifications section

Note: After the effective date of the PRCN, Silicon Labs reserves the right not to accept orders for the old revision.

Reason for Change

Hardware update to address a vulnerability. Details are covered in Security Advisory – A00000080.

Impact on Form, Fit, Function, Quality, Reliability

There is no impact on form, fit, quality or reliability.

Product Identification

Existing Part #	Replacement Part #	DropInCompInd.
WF200C	WF200D	Yes
WF200CR	WF200DR	Yes
WF200SC	WF200SD	Yes
WF200SCR	WF200SDR	Yes
WFM200S022XNA2	WFM200S022XNA3	Yes
WFM200S022XNA2R	WFM200S022XNA3R	Yes
WFM200S022XNN2	WFM200S022XNN3	Yes
WFM200S022XNN2R	WFM200S022XNN3R	Yes
WFM200SS22XNA2	WFM200SS22XNA3	Yes
WFM200SS22XNA2R	WFM200SS22XNA3R	Yes
WFM200SS22XNN2	WFM200SS22XNN3	Yes
WFM200SS22XNN2R	WFM200SS22XNN3R	Yes

Kit Identification

Kits impacted by the above product are listed below.
Orders for the following obsolete kits will no longer be accepted.

Existing Kit #	Replacement Kit #
NA	NA

Last Date of Unchanged Product: 12/28/2020

Qualification Samples

Available upon request.

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. <http://www.silabs.com/profile>

Qualification Data

See appendices.

Part Rev D, TSMC Fabrication, SPIL Assembly							
Test Name	Test Condition	Qualification	Lot ID or Start	Fail/Pass or End	Notes	Summary	Status
Test Group A – Accelerated Environment Stress Tests							
HAST	JA110 130°C, 85%RH Vcc=3.6V, 96 hours	3 lots, N=>25	Q042814	0/27	1	3 lots 0/81	Pass
			Q042698	0/27			
			Q042697	0/27			
UFAST	JA110 130°C, 85%RH 96 hours	3 lots, N=>77	Q042813	0/27	1	3 lots 0/81	Pass
			Q042703	0/27			
			Q042702	0/27			
Temp Cycle	JA104 Cond C: -65°C to 150°C 500 cycles	3 lots, N=>25	Q042812	0/27	1	3 lots 0/80	Pass
			Q042699	0/26			
			Q042696	0/27			
HTSL	JA103 150°C, 1000hr	3 lots, N=>25	Q042815	0/25	1	3 lots 0/79	Pass
			Q042701	0/27			
			Q042700	0/27			
Test Group B – Accelerated Lifetime Simulation Tests							
HTOL	JA108 T _J ≥ 125°C, Dynamic Vcc=3.6V, 1000 hours	3 lots, N=>77	Q042652	0/80		3 lots 0/240	Pass
			Q042651	0/80			
			Q043678	0/80			
LTOL	JA108 T _A = -10°C, Dynamic Vcc=3.6V, 1000 hours	1 lot, N=>32	Q042766	0/40		1 lots 0/40	Pass
ELFR	JA108 T _J ≥ 125°C, Dynamic Vcc=3.6V, 48 hours	3 lots, N=>500	Q042650	0/520		4 lots 0/1642	Pass
			Q046534	0/80			
			Q043529	0/520			
			Q042649	0/522			
Test Group E – Electrical Verification							
ESD-HBM	JS-001	1 lot, N=>3	Q042644			2.5 kV	Class 2
ESD-CDM	JS-002	1 lot, N=>3	Q046533			750 V	Class C2b
Latch Up	JESD78 ±200mA	1 lot, N=>3	Q042645 Q042646	25 °C 125 °C			Pass
Notes:							
1. Parts are Pre-conditioned at MSL 1/260°C							
This report applies to the following part numbers:							
WF200D	WF200SD						

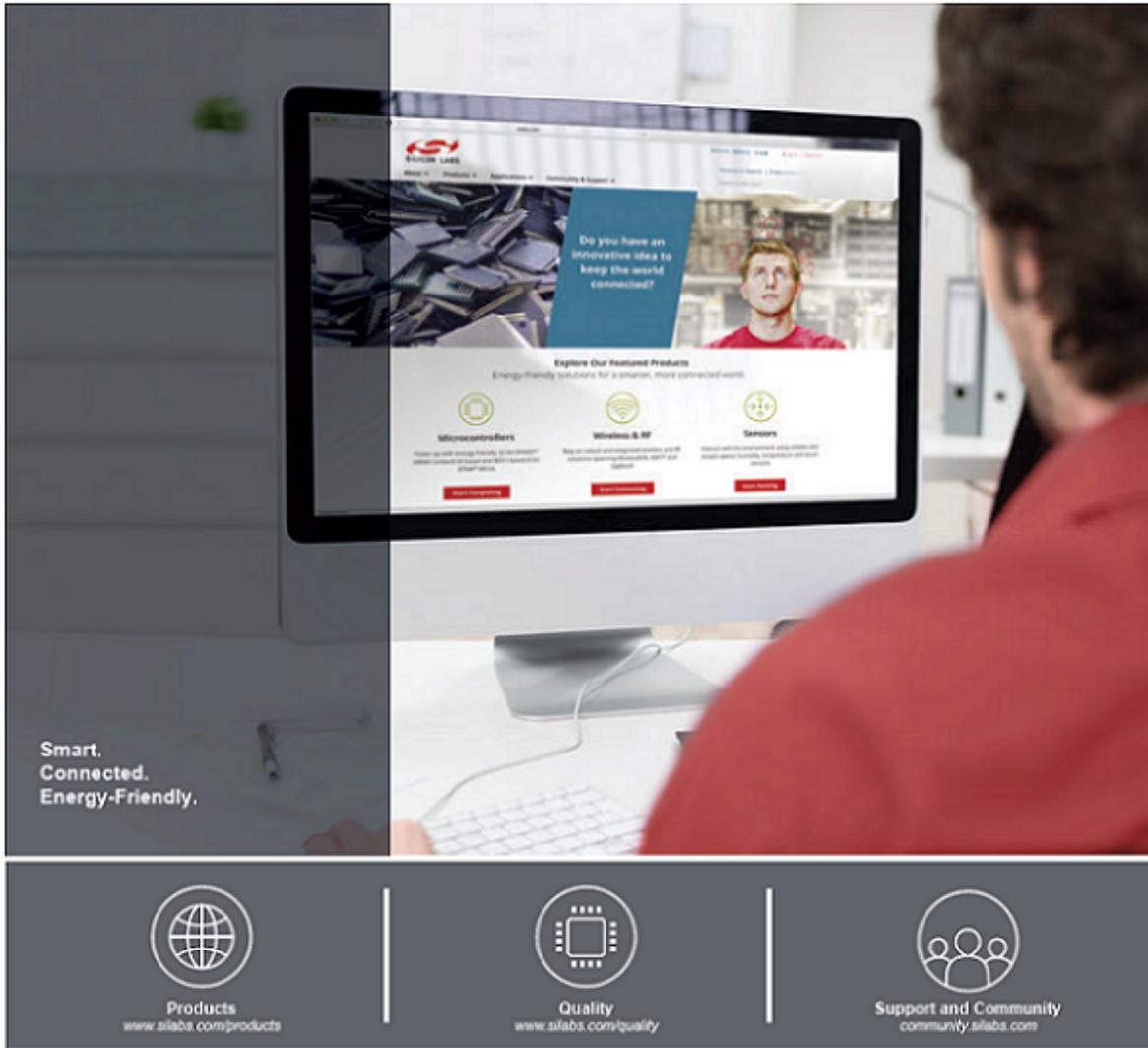
Appendix 1. WF200 Qualification Report

Part Rev A3, TSMC Fabrication, ASEKR Assembly except as noted							
Test Name	Test Condition	Qualification	Lot ID or Start	Fail/Pass or End	Notes	Summary	Status
Test Group A – Accelerated Environment Stress Tests							
THB	JA101 85°C, 85%RH Vcc=3.6V, 1000 hours	3 lots, N=>25	Q044243	0/25	1	3 lots 0/75	Pass
			Q044255	0/25	1		
			Q044256	0/25	1		
Temp Cycle	JA104 Cond G: -40°C to 125°C 850 cycles	3 lots, N=>25	Q044242	0/25	1	3 lots 0/75	Pass
			Q044258	0/25	1		
			Q044257	0/25	1		
HTSL	JA103 150°C, 1000hr	3 lots, N=>25	Q044241	0/25	1	3 lots 0/75	Pass
			Q044260	0/25	1		
			Q044259	0/25	1		
Test Group B – Accelerated Lifetime Simulation Tests							
HTOL	JA108 T _J ≥ 125°C, Dynamic Vcc=3.6V, 1000 hours	3 lots, N=>77	Q042651	0/80		3 lots 0/240	Pass
			Q042652	0/80			
			Q043678	0/80			
LTOL	JA108 T _A = -10°C, Dynamic Vcc=3.6V, 1000 hours	1 lot, N=>32	Q042766	0/40		1 lots 0/40	Pass
ELFR	JA108 T _J ≥ 125°C, Dynamic Vcc=3.6V, 48 hours	3 lots, N=>500	Q042649	0/522		3 lots 0/1562	Pass
			Q042650	0/520			
			Q043529	0/520			
Test Group E – Electrical Verification							
ESD-HBM	JS-001	1 lot, N=>3	Q044603			2 kV	Class 2
ESD-CDM	JESD22-C101	1 lot, N=>3	Q046396			500 V	Class III
Latch Up	JESD78 ±100mA Overvoltage = 3.6V	1 lot, N=>3 1 lot, N=>3	Q044152	25 °C			Pass
			Q044153	125 °C			Pass
Notes:							
1. Parts are Pre-conditioned at MSL3/260°C							
This report applies to the following part numbers:							
WFM200SS22XNA3/R							
WFM200S022XNA3/R							

Appendix 2. WFM200 Antenna Qualification Report

Part Rev A3, TSMC Fabrication, ASEKR Assembly except as noted							
Test Name	Test Condition	Qualification	Lot ID or Start	Fail/Pass or End	Notes	Summary	Status
Test Group A – Accelerated Environment Stress Tests							
THB	JA101 85°C, 85%RH Vcc=3.6V, 1000 hours	3 lots, N=>25	Q044243	0/25	1	3 lots 0/75	Pass
			Q044255	0/25	1		
			Q044256	0/25	1		
Temp Cycle	JA104 Cond G: -40°C to 125°C 850 cycles	3 lots, N=>25	Q044242	0/25	1	3 lots 0/75	Pass
			Q044258	0/25	1		
			Q044244	0/25	1		
HTSL	JA103 150°C, 1000hr	3 lots, N=>25	Q044241	0/25	1	3 lots 0/75	Pass
			Q044260	0/25	1		
			Q044259	0/25	1		
Test Group B – Accelerated Lifetime Simulation Tests							
HTOL	JA108 T _j ≥ 125°C, Dynamic Vcc=3.6V, 1000 hours	3 lots, N=>77	Q042651	0/80		3 lots 0/240	Pass
			Q042652	0/80			
			Q043678	0/80			
LTOL	JA108 T _A = -10°C, Dynamic Vcc=3.6V, 1000 hours	1 lot, N=>32	Q042766	0/40		1 lots 0/40	Pass
ELFR	JA108 T _j ≥ 125°C, Dynamic Vcc=3.6V, 48 hours	3 lots, N=>500	Q042649	0/522		3 lots 0/1562	Pass
			Q042650	0/520			
			Q043529	0/520			
Test Group E – Electrical Verification							
ESD-HBM	JS-001	1 lot, N=>3	Q044603			2 kV	Class 2
ESD-CDM	JESD22-C101	1 lot, N=>3	Q046396			500 V	Class III
Latch Up	JESD78 ±100mA Overvoltage = 3.6V	1 lot, N=>3 1 lot, N=>3	Q044152	25 °C			Pass
			Q044153	125 °C			Pass
Notes:							
1. Parts are Pre-conditioned at MSL3/260°C							
This report applies to the following part numbers:							
WFM200SS22XNN3/R							
WFM200S022XNN3/R							

Appendix 3. WFM200 Non-Antenna Qualification Report



Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISModem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701

<http://www.silabs.com>