

# PI5C3384 PI5C3384C PI5C32384 (25Ω)

# 10-Bit, 2-Port Bus Switch

## **Features:**

- Near-Zero propagation delay
- Low noise,  $25\Omega$  version (PI5C32384)
- $5\Omega$  switches connect inputs to outputs (PI5C3384)
- Direct bus connection when switches are ON
- Ultra-low quiescent power (0.2 μA typical)
  Ideally suited for notebook applications
- Packaging (Pb-free & Green Available):
  - 24-pin 150-mil wide plastic QSOP (Q)
  - 24-pin 300-mil wide plastic SOIC (S)
  - 24-pin 173-mil wide plastic TSSOP (L)

## **Block Diagram**



## Truth Table<sup>(1)</sup>

Function	<b>BE</b> A	<b>BE</b> <sub>B</sub>	B <sub>0</sub> -B <sub>4</sub>	B5-B9
Disconnect	Н	Н	Hi-Z	Hi-Z
Connect	L	Н	A <sub>0</sub> -A <sub>4</sub>	Hi-Z
Connect	Н	L	Hi-Z	A5-A9
Connect	L	L	A <sub>0</sub> -A <sub>4</sub>	A5-A9

#### Note:

- 1. H = High Voltage Level
- X = Don't Care
- L = Low Voltage Level
- Hi-Z = High Impedance

## **Description:**

Pericom Semiconductor's PI5C3384, PI5C3384C, and PI5C32384 are 10-bit, 2-port bus switches designed with a low On-Resistance allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable ( $\overline{BE}$ ) input signal. Two bus enable signals are provided, one for each of the upper and lower five bits of the two 10-bit buses. The PI5C32384 is designed with an internal 25 $\Omega$  resistor reducing noise reflection in high-speed applications.

## **Pin Configuration**



### **Pin Description**

in Description				
Pin Name	Description			
$\overline{\text{BE}}_{\text{A}}, \overline{\text{BE}}_{\text{B}}$	Bus Enable Inputs (Active LOW)			
A <sub>0</sub> -A <sub>9</sub>	Bus A			
B <sub>0</sub> -A <sub>9</sub>	Bus B			
GND	Ground			
V <sub>CC</sub>	Power			



### **Maximum Ratings**

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C to +150°C
Ambient Temperature with Power Applied40°C to +85°C
Supply Voltage to Ground Potential (Inputs & $V_{CC}Only)\ldots\ldots -0.5V$ to $+7.0V$
Supply Voltage to Ground Potential (Outputs & D/O Only)0.5V to +7.0V
DC Input Voltage
DC Output Current
Power Dissipation0.5W

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Note:

Parameters	Description	Test Co	Test Conditions <sup>(1)</sup>		Typ <sup>(2)</sup>	Max.	Units
V <sub>IH</sub>	Input HIGH Voltage	Guaranteed Logic HI	Guaranteed Logic HIGH Level				v
V <sub>IL</sub>	Input LOW Voltage	Guaranteed Logic LO	DW Level	-0.5			
I <sub>IH</sub>	Input HIGH Current	$V_{CC} = Max., V_{IN} = V$	/сс				
I <sub>IL</sub>	Input LOW Current	$V_{\rm CC}$ = Max., $V_{\rm IN}$ = 0	GND				μA
I <sub>OZH</sub>	High-Impedance Output Current	$0 \le A, B \le V_{CC}$					μΛ
			PI5C3384		-0.7	-1.2	
V <sub>IK</sub>	V <sub>IK</sub> Clamp Diode Voltage	$V_{CC} = Min., I_{IN} =$ -18 mA	PI5C3384C			-1.8	V
			PI5C32384				
IOS	Short Circuit Current <sup>(3)</sup>	A(B) = 0V, B(A) = 0	V <sub>CC</sub>	100			mA
V <sub>H</sub>	Input Hysteresis at Control Pins				150		mV
		$V_{CC} = Min.,$	PI5C3384		5	7	
		$V_{\rm IN} = 0V$ ,	PI5C3384C	-	5	7	1
	$G : (1 \cap \mathbf{P}) : (4)$	$I_{ON} = 48 \text{ mA}$	PI5C32384	20	28	40	
R <sub>ON</sub>	Switch On-Resistance <sup>(4)</sup>	$V_{CC} = Min.,$	PI5C3384		10	15	Ω
		$V_{IN} = 2.4V,$	PI5C3384C	-	10	15	]
		$I_{ON} = 15 mA$	PI5C32384	20	35	48	1

#### **Capacitance** ( $T_A = 25^{\circ}C$ , f = 1 MHz)

Parameters <sup>(5)</sup>	Description	Test Conditions	Тур	Max.	Units
C <sub>IN</sub>	Input Capacitance			6	
C <sub>OFF</sub>	A/B Capacitance, Switch Off	$V_{IN} = 0V$	_	6	pF
C <sub>ON</sub>	A/B Capacitance, Switch On		_	8	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at  $V_{CC}$  = 5.0V,  $T_A$  = 25°C ambient and maximum loading.

3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

4. Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (A,B) pins.

5. This parameter is determined by device characterization but is not production tested.

Parameters	Description	Test Conditio	ons <sup>(1)</sup>	Min.	<b>Typ</b> <sup>(2)</sup>	Max.	Units
I <sub>CC</sub>	Quiescent Power	$V_{CC} = Max.,$ $V_{IN} = GND \text{ or } V_{CC}$	PI5C3384 PI5C32384	_	0.1	3.0	μA
	Supply Current	$v_{IN} = GIND \text{ or } v_{CC}$	PI5C3384C	_	60	100	
ΔI <sub>CC</sub>	Supply Current per Input @ TTL HIGH	$V_{CC} = Max.,$ $V_{IN} = 3.4V^{(3)}$			—	2.5	mA
ICCD	Supply Current per Input per MHz <sup>(4)</sup>	$V_{CC} = Max.,$ A and B Pins Open BE1 or BE2 = GND Control Input Toggling 50% Duty Cycle			_	0.25	mA/ MHz

#### **Power Supply Characteristics**

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.

2. Typical values are at  $V_{CC} = 5.0V$ ,  $+25^{\circ}C$  ambient.

3. Per TTL driven input ( $V_{IN}$  = 3.4V, control inputs only); A and B pins do not contribute to I<sub>CC</sub>.

4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Parameters	Description	<b>Conditions</b> <sup>(1)</sup>	PI5C3384/3384C	4C	Unit	
r ar ameter s	Description	Conditions	Min	Тур	Max	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay <sup>(2,3)</sup> Ax to Bx, Bx to Ax			0.25	_	
t <sub>PZH</sub> t <sub>PZL</sub>	Bus Enable Time BEx to Ax or Bx	$C_{L} = 50 pF$ $R_{L} = 500 \Omega$	1.5	_	6.5	ns
t <sub>PHL</sub> t <sub>PLZ</sub>	Bus Disable Time BEx to Ax or Bx		1.5	—	5.5	

### PI5C3384/3384C Switching Characteristics over Operating Range

Notes:

1. See test circuit and waveforms.

2. This parameter is guaranteed but not tested on Propagation Delays.

3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## PI5C32384 Switching Characteristics over Operating Range

Parameters	Description	Conditions <sup>(1)</sup>	Conditions <sup>(1)</sup> PI5C32384		Unit	
rarameters		Min	Тур	Max	Unit	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay <sup>(2, 3)</sup> Ax to Bx, Bx to Ax			1.25		
t <sub>PZH</sub> t <sub>PZL</sub>	Bus Enable Time BEx to Ax or Bx	$C_{L} = 50 pF$ $R_{L} = 500 \Omega$	1.5	_	7.5	ns
t <sub>PHZ</sub> t <sub>PLZ</sub>	Bus Disable Time BEx to Ax or Bx		1.5		5.5	

Notes:

1. See test circuit and waveforms.

2. This parameter is guaranteed but not tested on Propagation Delays.

3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.



## Packaging Mechanical: 24-pin TSSOP (L)



# Packaging Mechanical: 24-pin QSOP (Q)





## Packaging Mechanical: 24-pin SOIC (S)



### **Ordering Information**

<b>Ordering Code</b>	Package Code	Package Description		
PI5C3384L	L	24-pin 173-mil wide plastic TSSOP		
PI5C3384LE	L	Pb-free & Green, 24-pin 173-mil wide plastic TSSOP		
PI5C3384Q	Q	24-pin 150-mil wide plastic QSOP		
PI5C3384QE	Q	Pb-free & Green, 24-pin 150-mil wide plastic QSOP		
PI5C3384S	S	24-pin 300-mil wide plastic SOIC		
PI5C3384SE	S	Pb-free & Green, 24-pin 300-mil wide plastic SOIC		
PI5C32384L	L	24-pin 173-mil wide plastic TSSOP		
PI5C32384LE	L	Pb-free & Green, 24-pin 173-mil wide plastic TSSO		
PI5C3384CL	L	24-pin 173-mil wide plastic TSSOP		
PI5C3384CLE	L	Pb-free & Green, 24-pin 173-mil wide plastic TSSOP		
PI5C3384CQ	Q	24-pin 150-mil wide plastic QSOP		
PI5C3384CQE	Q	Pb-free & Green, 24-pin 150-mil wide plastic QSOP		

#### Notes:

1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/

- 2. X = Tape/Reel
- 3. Number of Transistors = TBD