



2206211282 MGM13S and BGM13S Datasheet Release v1.3

PCN Issue Date: Jun 21, 2022

Effective Date: Sep 27, 2022

PCN Type: Datasheet

Description of Change

Silicon Labs is pleased to announce the release of datasheet version 1.3 for MGM13S and BGM13S devices.

Notable changes from previous revisions are:

- Added timing specifications for RESETn low time in Table 4.28 General-Purpose I/O (GPIO) on page 43.
- Removed BIASPROG = 1, FULLBIAS = 0 specifications from Table 4.31 Analog Comparator (ACMP) on page 48.
- Removed all references to RFSENSE
- Removed all references to BOOT_TX and BOOT_RX. (MGM13S only)

Reason for Change

Improve our production capability

Impact on Form, Fit, Function, Quality, Reliability

There is no change in form, fit, quality and reliability. Functionality is impacted as described in the description.

Product Identification

This notification includes both standard and customer-specific part numbers. An asterisk * represents a number or letter (one or more) in a customer-specific part number.

Existing Part #
BGM13S22F512GA-V3
BGM13S22F512GA-V3R
BGM13S22F512GN-V3
BGM13S22F512GN-V3R
BGM13S32F512GA-V3
BGM13S32F512GA-V3R
BGM13S32F512GN-V3
BGM13S32F512GN-V3R
BGM13S22P****GN-V3
BGM13S22P****GN-V3R
BGM13S22P****GA-V3
BGM13S22P****GA-V3R
BGM13S32P****GA-V3
BGM13S32P****GA-V3R
MGM13S02F512GA-V3
MGM13S02F512GA-V3R
MGM13S02F512GN-V3
MGM13S02F512GN-V3R
MGM13S12F512GA-V3
MGM13S12F512GA-V3R
MGM13S12F512GN-V3
MGM13S12F512GN-V3R
MGM13S12P****GA-V3
MGM13S12P****GA-V3R

Last Date of Unchanged Product: Sep 27, 2022

Qualification Samples

NA

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

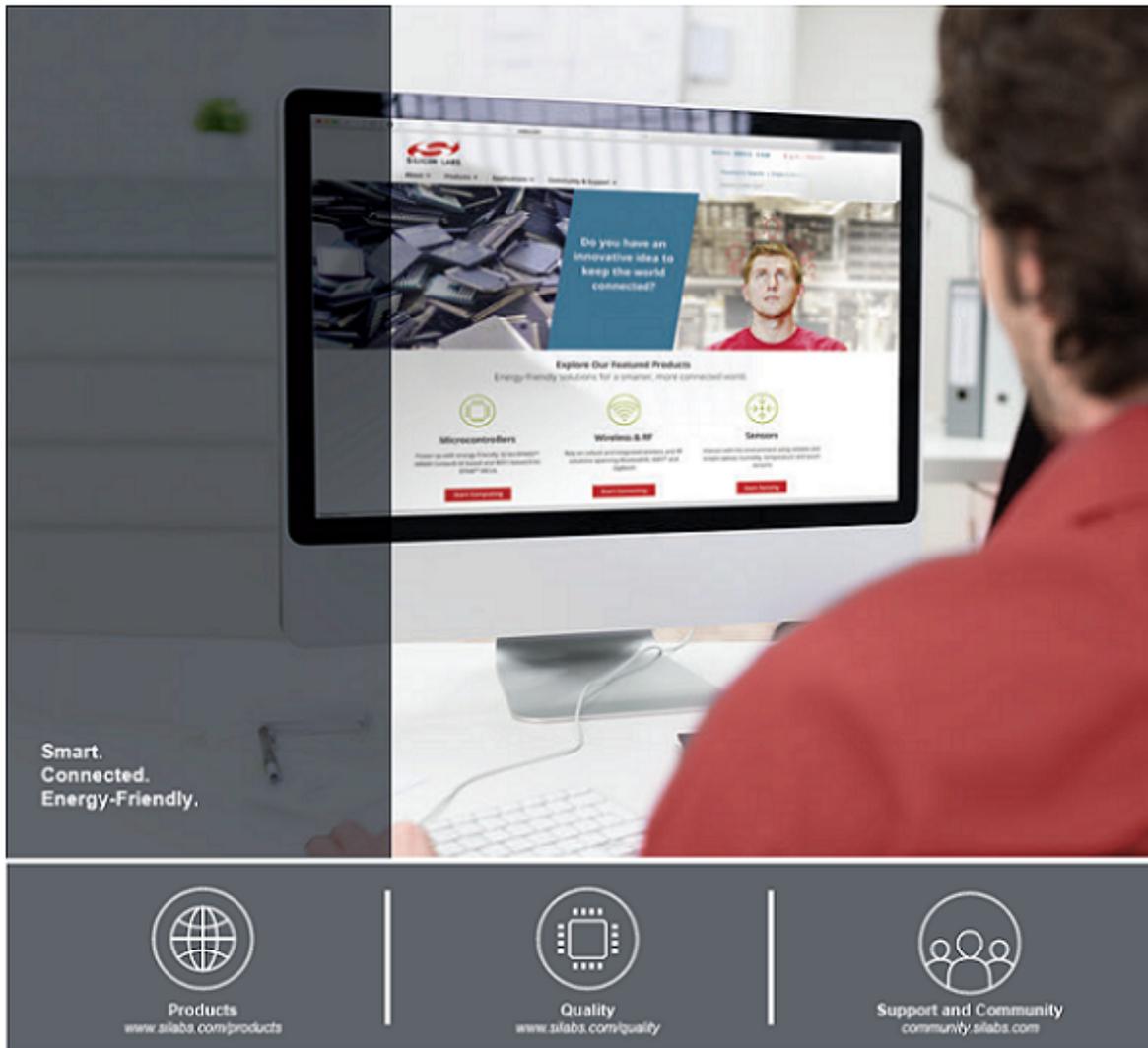
Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, “how-to” and design documents, product change notices (PCN) and other valuable content available only to registered users. <http://www.silabs.com/profile>

Qualification Data

NA



Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOModem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701

<http://www.silabs.com>