RoHS

COMPLIANT HALOGEN

FREE

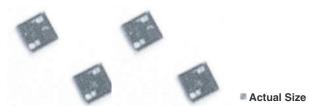
GREEN

(5-2008)



Vishay Sfernice

Precision Wirebondable Single Value Thin Film Chip Resistor



LINKS TO ADDITIONAL RESOURCES



FEATURES

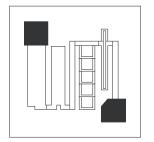
- Small size 20 mils x 20 mils
- Low temperature coefficient 25 ppm/°C
- Excellent stability 0.05 % (2000 h, rated power at +70 °C)
- Wirebondable
- Tolerance down to 0.1 %
- High temperature (230 °C), see RMKHT datasheet (www.vishay.com/doc?60075)
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

The demand for high precision, high stability microchips for both military and industrial environments is increasing with the growth and sophistication of modern hybrid circuitry.

The RSK 22 series are single value resistor chips. They provide excellent long term stability ± 0.05 % (2000 h, rated power, at +70 °C) and low noise characteristics < 35 dB.

SCHEMATIC AND PATTERN



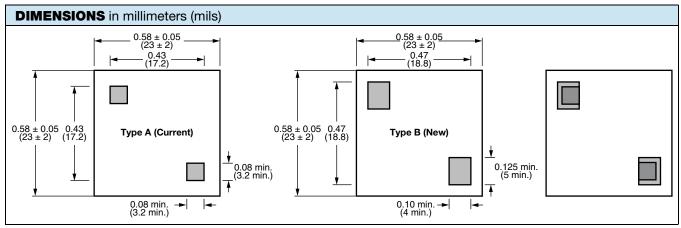


STANDARD ELECTRICAL SPECIFICATIONS								
MODEL	SIZE	RESISTANCE RANGE Ω	RATED POWER P _{70 °C} W	LIMITING ELEMENT VOLTAGE V	TOLERANCE ± %	TEMPERATURE COEFFICIENT ± ppm/°C		
RSK 22N	0202	10 to 500K	0.05	100	0.1, 0.5, 1	25		

CLIMATIC SPECIFICATIONS				
Operating temperature range (1)	-55 °C to +155 °C			
Storage temperature range	-55 °C to +155 °C			

Note

MECHANICAL SPECIFICATIONS				
Resistive element	Nichrome			
Passivation	Silicon nitride			
Substrate material	Silicon			
Bonding pads	Aluminum			

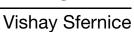


Note

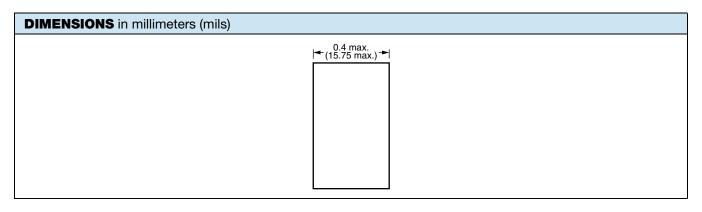
Revision: 20-May-2020

Customer can get one or the other part, but positions of pads are similar

⁽¹⁾ For temperature up to 200 °C, please consult factory

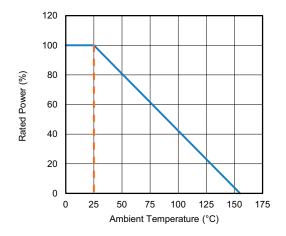


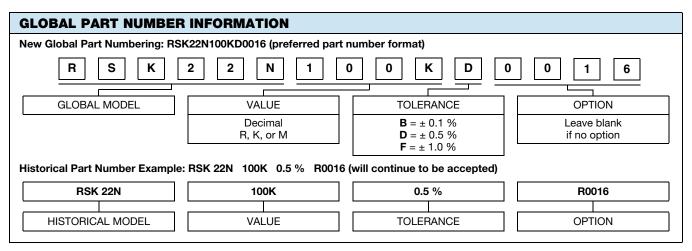




TECHNICAL SPECIFICATIONS					
TEST	SPECIFICATIONS	CONDITIONS			
Stability	± 0.05 % typical, ± 0.1 % maximum	2000 h at +70 °C under Pn			
Voltage coefficient	< 0.1 ppm/V				
Noise	< -35 dB typical	MIL-STD-202 method 308			
Thermal EMF	0.01 μV/°C				
Shelf life stability	< 50 ppm				

DERATING







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Vishay

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