FAIRCHILD

SEMICONDUCTOR

DM74ALS153 Dual 1-of-4 Line Data Selector/Multiplexer

General Description

This Data Selector/Multiplexer contains full on-chip decoding to select one-of-four data sources as a result of a unique two-bit binary code at the Select inputs. Each of the two Data Selector/Multiplexer circuits have their own separate Data and Strobe inputs and a non-inverting output buffer. The Select inputs A and B are common to both sections. The Strobe inputs, when at the HIGH level, disable their associated data inputs and force the corresponding output to the LOW state. The Select input buffers incorporate internal overlap features to ensure that select input changes do not cause invalid output transients.

Features

Advanced oxide-isolated, ion-implanted Schottky TTL process

April 1984

Revised February 2000

- Switching performance is guaranteed over full temperature and V_{CC} supply range
- Pin and functional compatible with LS family counterpart
- Improved output transient handling capability

Ordering Code:

Order Number	Package Number	Package Description					
DM74ALS153M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow					
DM74ALS153SJ	M16D	16-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide					
DM74ALS153N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide					
Devices also available	Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.						





Function Table

Select Inputs		Data Inputs				Strobe	Output
В	Α	C0	C1	C2	C3	G	Y
Х	Х	Х	Х	Х	Х	Н	L
L	L	L	Х	Х	х	L	L
L	L	н	Х	Х	х	L	н
L	н	Х	L	Х	х	L	L
L	н	Х	н	Х	х	L	н
н	L	Х	х	L	х	L	L
н	L	Х	х	н	х	L	н
н	н	Х	Х	Х	L	L	L
н	н	Х	Х	х	н	L	н

Select inputs A and B are common to both section

H = HIGH Level L = LOW Level

X = Don't Care

© 2000 Fairchild Semiconductor Corporation DS006204





Absolute Maximum Ratings(Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	$-65^{\circ}C$ to $+150^{\circ}C$
Typical θ _{JA}	
N Package	78.0°C/W
M Package	107.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
Vcc	Supply Voltage	4.5	5	5.5	V
/ _{IH}	HIGH Level Input Voltage	2			V
/ _{IL}	LOW Level Input Voltage			0.8	V
он	HIGH Level Output Current			-2.6	mA
OL	LOW Level Output Current			24	mA
Γ _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range. All typical values are measured at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V, I_{IN} = -18 \text{ mA}$	$V_{CC} = 4.5V, I_{IN} = -18 \text{ mA}$			-1.5	V
V _{OH}	HIGH Level	$V_{CC} = 4.5V$, $I_{OH} = Max$	$V_{CC} = 4.5V$, $I_{OH} = Max$		3.2		V
	Output Voltage	I_{OH} = –400 $\mu\text{A}, \ V_{CC}$ = 4.5V to 5.5V		$V_{CC} - 2$			V
V _{OL}	LOW Level Output Voltage	$V_{CC} = 4.5V$	I _{OL} = 24 mA		0.35	0.5	V
I _I	Input Current at Max Input Voltage	$V_{CC} = 5.5V, V_{IN} = 7V$	·			0.1	mA
I _{IH}	High Level Input Current	$V_{CC} = 5.5V, V_{IN} = 2.7V$	$V_{CC} = 5.5V, V_{IN} = 2.7V$			20	μΑ
IIL	Low Level Input Current	$V_{CC} = 5.5V, V_{IN} = 0.4V$				-0.1	mA
I _O	Output Drive Current	$V_{CC} = 5.5 V, V_{OUT} = 2.25 V$		-30		-112	mA
I _{CC}	Supply Current	$V_{CC} = 5.5V$ All Inputs = 4.5V			7.5	14	mA

Switching Characteristics

Symbol	Parameter	Conditions	From	То	Min	Max	Units
t _{PLH}	Propagation Delay Time LOW-to-HIGH Level Output	$V_{CC} = 4.5V$ to 5.5V $C_L = 50 \text{ pF}$	Select	Y	5	21	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output	$R_L = 500\Omega$	Select	Y	5	21	ns
t _{PLH}	Propagation Delay Time LOW-to-HIGH Level Output		Data	Y	3	10	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output		Data	Y	4	15	ns
t _{PLH}	Propagation Delay Time LOW-to-HIGH Level Output		Strobe	Y	5	18	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output		Strobe	Y	5	18	ns





